

ART2K5TFUS; ART2K5TFUG

Power LDMOS transistor

Rev. 1 — 8 February 2024

AMPLEON

Product data sheet

1. Product profile

1.1 General description

Based on Advanced Rugged Technology (ART), this 2500 W LDMOS RF power transistor has been designed to cover a wide range of applications for ISM, broadcast and communications. The unmatched transistor has a frequency range of 1 MHz to 400 MHz.

Table 1. Application information

Test signal	f	V _{DS}	P _L	G _p	η _D
	(MHz)	(V)	(W)	(dB)	(%)
CW pulsed [1]	108	75	2500	29.5	76

[1] Test circuit: $t_p = 100 \mu\text{s}$; $\delta = 10 \%$.

1.2 Features and benefits

- Qualified to $V_{DS} = 75 \text{ V}$
- Integrated thermal sensor
- Integrated dual sided ESD protection enables class C operation and complete switch off of the transistor
- Excellent ruggedness with no device degradation
- High efficiency
- Excellent thermal stability
- Designed for broadband operation
- For RoHS compliance see the product details on the Ampleon website

1.3 Applications

- Industrial, scientific and medical applications
 - ◆ Plasma generators
 - ◆ MRI systems
 - ◆ CO₂ lasers
 - ◆ Particle accelerators
- Broadcast
 - ◆ FM radio
 - ◆ VHF TV
- Communications
 - ◆ Non cellular communications
 - ◆ UHF radar

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
ART2K5TFUS (ACC-1230-6F-2)			
1	drain1		<p style="text-align: right; font-size: small;">amp01794</p>
2	drain2		
3	gate1		
4	gate2		
5	source [1]		
6	temperature sense FET1 [2]		
7	temperature sense FET2 [2]		
ART2K5TFUG (ACC-1230-6G-2)			
1	drain1		<p style="text-align: right; font-size: small;">amp01794</p>
2	drain2		
3	gate1		
4	gate2		
5	source [1]		
6	temperature sense FET1 [2]		
7	temperature sense FET2 [2]		

[1] Connected to flange.

[2] The ART2K5TFUS and ART2K5TFUG are equipped with a thermal sense FET and can be used to sense the die temperature during operation of the device. This thermal FET is electrically disconnected from the RF power FETs on the die and share only a common ground. The sensor is operated by applying a fixed voltage to its input pin and monitor the current, which is temperature depended.

3. Ordering information

Table 3. Ordering information

Package name	Orderable part number	12NC	Packing description	Min. orderable quantity (pieces)
ACC-1230-6F-2	ART2K5TFUSJ	9349 607 39118	TR13; 100-fold; 56 mm; non-dry pack	100
ACC-1230-6G-2	ART2K5TFUGJ	9349 607 40118	TR13; 100-fold; 56 mm; non-dry pack	100

4. Limiting values

Table 4. Limiting values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	[1]	-	200	V
V_{GS}	gate-source voltage		-9	+13	V
V_{TS}	temperature sensor voltage		-9	+5.5	V
T_{stg}	storage temperature		-65	+150	°C
T_j	junction temperature	[2]	-	225	°C

[1] Specified over lifetime at maximum operating temperature.

[2] Continuous use at maximum temperature will affect the reliability, for details refer to the online MTF calculator.

5. Thermal characteristics

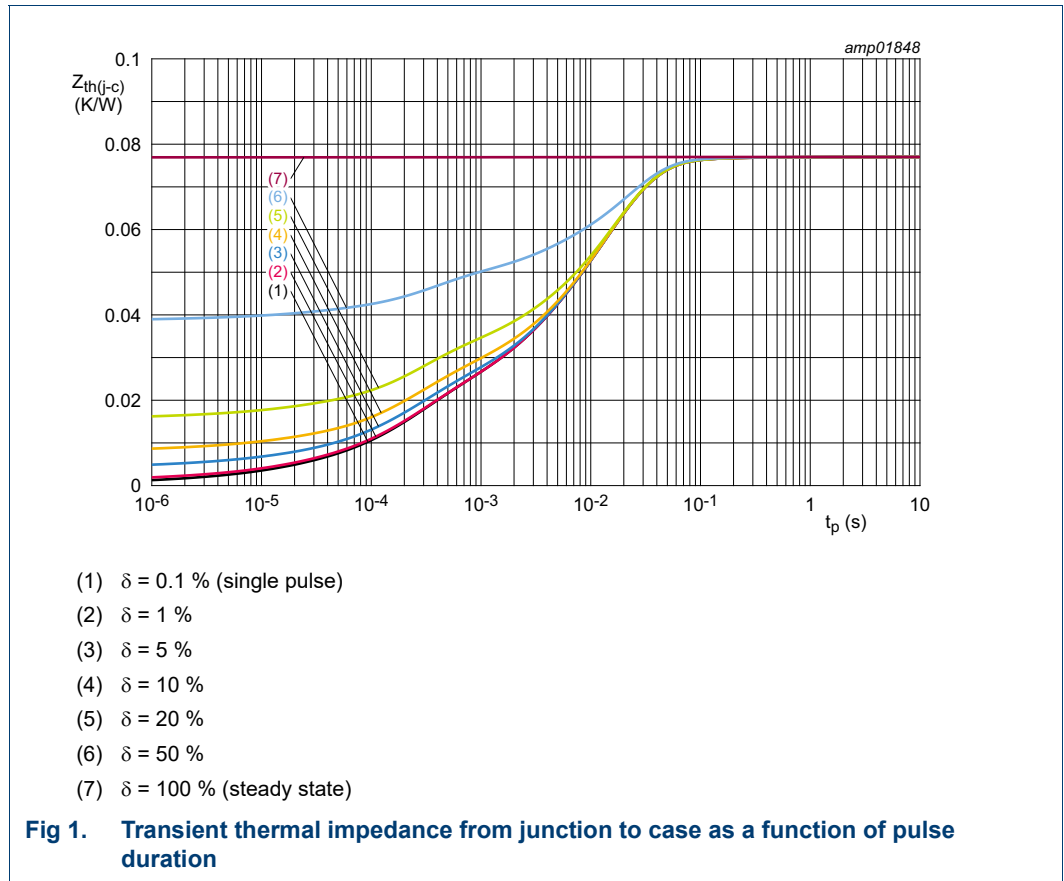
Table 5. Thermal characteristics

According to standard MIL-STD-883E.

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-c)}$	thermal resistance from junction to case	$T_j = 95\text{ °C}$, measured under RF condition	[1][2] 0.077	K/W

[1] Refer to application note AN221014 on the Ampleon website.

[2] See [Figure 1](#).



6. Characteristics

Table 6. DC characteristics

$T_j = 25\text{ }^\circ\text{C}$; per section unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0\text{ V}; I_D = 5.5\text{ mA}$	203	208	-	V
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS} = 20\text{ V}; I_D = 550\text{ mA}$	1.6	2.1	2.6	V
$V_{TS(th)}$	temperature sensor threshold voltage	$I_{TS} = 0.7\text{ mA}$	1.75	2.3	2.75	V
I_{DSS}	drain leakage current	$V_{GS} = 0\text{ V}; V_{DS} = 75\text{ V}$	-	-	2.8	μA
I_{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75\text{ V}; V_{DS} = 20\text{ V}$	-	77	-	A
I_{GSS}	gate leakage current	$V_{GS} = 13\text{ V}; V_{DS} = 0\text{ V}$	-	-	280	nA
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75\text{ V}; I_D = 19.25\text{ A}$	-	0.100	-	Ω

Table 7. AC characteristics

$T_j = 25\text{ }^\circ\text{C}$; per section unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
C_{rs}	feedback capacitance	$V_{GS} = 0\text{ V}$; $V_{DS} = 75\text{ V}$; $f = 1\text{ MHz}$	-	1.75	-	pF
C_{iss}	input capacitance	$V_{GS} = 0\text{ V}$; $V_{DS} = 75\text{ V}$; $f = 1\text{ MHz}$	-	598	-	pF
C_{oss}	output capacitance	$V_{GS} = 0\text{ V}$; $V_{DS} = 75\text{ V}$; $f = 1\text{ MHz}$	-	167	-	pF

Table 8. RF characteristics

Test signal: pulsed RF; $t_p = 100\text{ }\mu\text{s}$; $\delta = 3\%$; $f = 108\text{ MHz}$; RF performance at $V_{DS} = 75\text{ V}$; $I_{Dq} = 50\text{ mA}$ per section; $T_{case} = 25\text{ }^\circ\text{C}$; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
G_p	power gain	$P_L = 2500\text{ W}$	28.5	29.8	-	dB
RL_{in}	input return loss	$P_L = 2500\text{ W}$	-	-15	-	dB
η_D	drain efficiency	$P_L = 2500\text{ W}$	71	74	-	%

7. Test information

7.1 Ruggedness in class-AB operation

The ART2K5TFUS and ART2K5TFUG are capable of withstanding a load mismatch corresponding to $VSWR \geq 65 : 1$ through all phases under the following conditions: $V_{DS} = 75\text{ V}$; $I_{Dq} = 100\text{ mA}$ per section; $P_L = 2500\text{ W}$ pulsed; $t_p = 100\text{ }\mu\text{s}$; $\delta = 10\%$; $f = 108\text{ MHz}$.

7.2 Impedance information

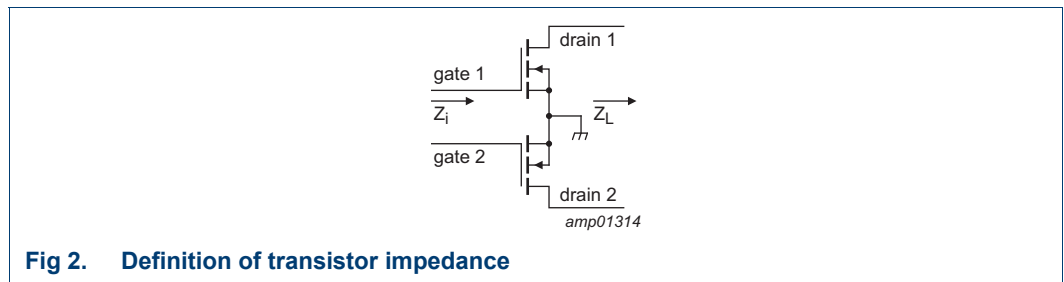


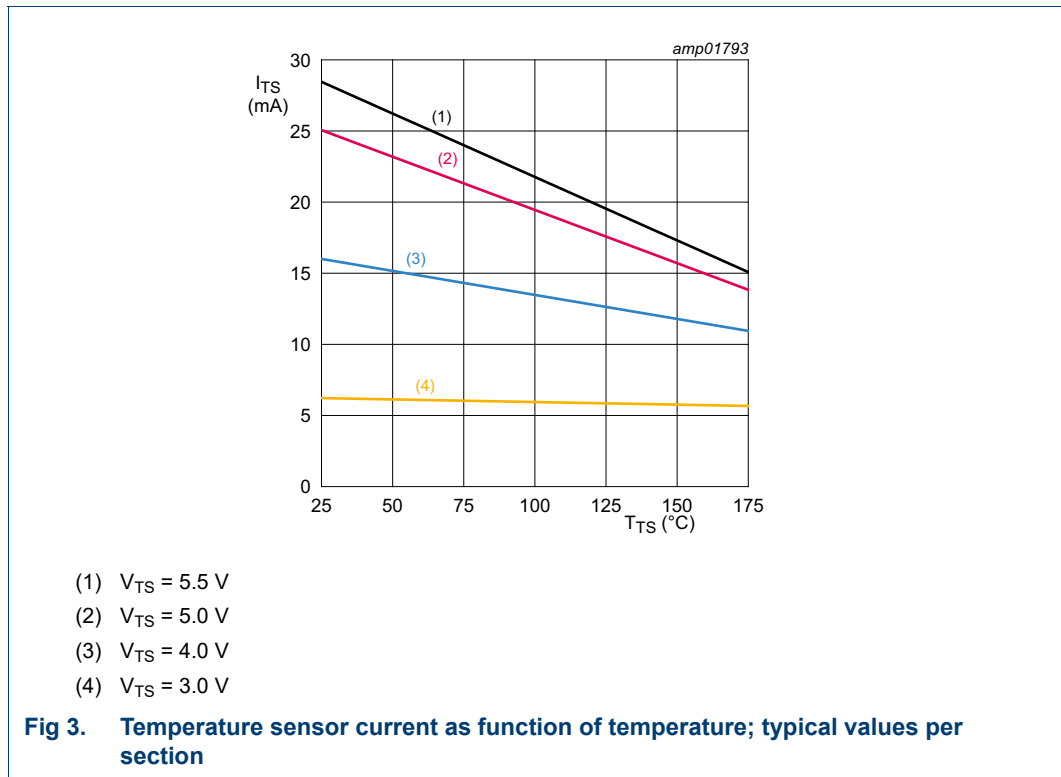
Fig 2. Definition of transistor impedance

Table 9. Typical push-pull impedance

Simulated Z_i and Z_L device impedance; impedance info at $V_{DS} = 75\text{ V}$ and $P_L = 2500\text{ W}$.

f (MHz)	Z_i (Ω)	Z_L (Ω)
108	$2.4 - j8.9$	$4.1 + j1.0$

7.3 Graphical data thermal sensor



7.4 Test circuit

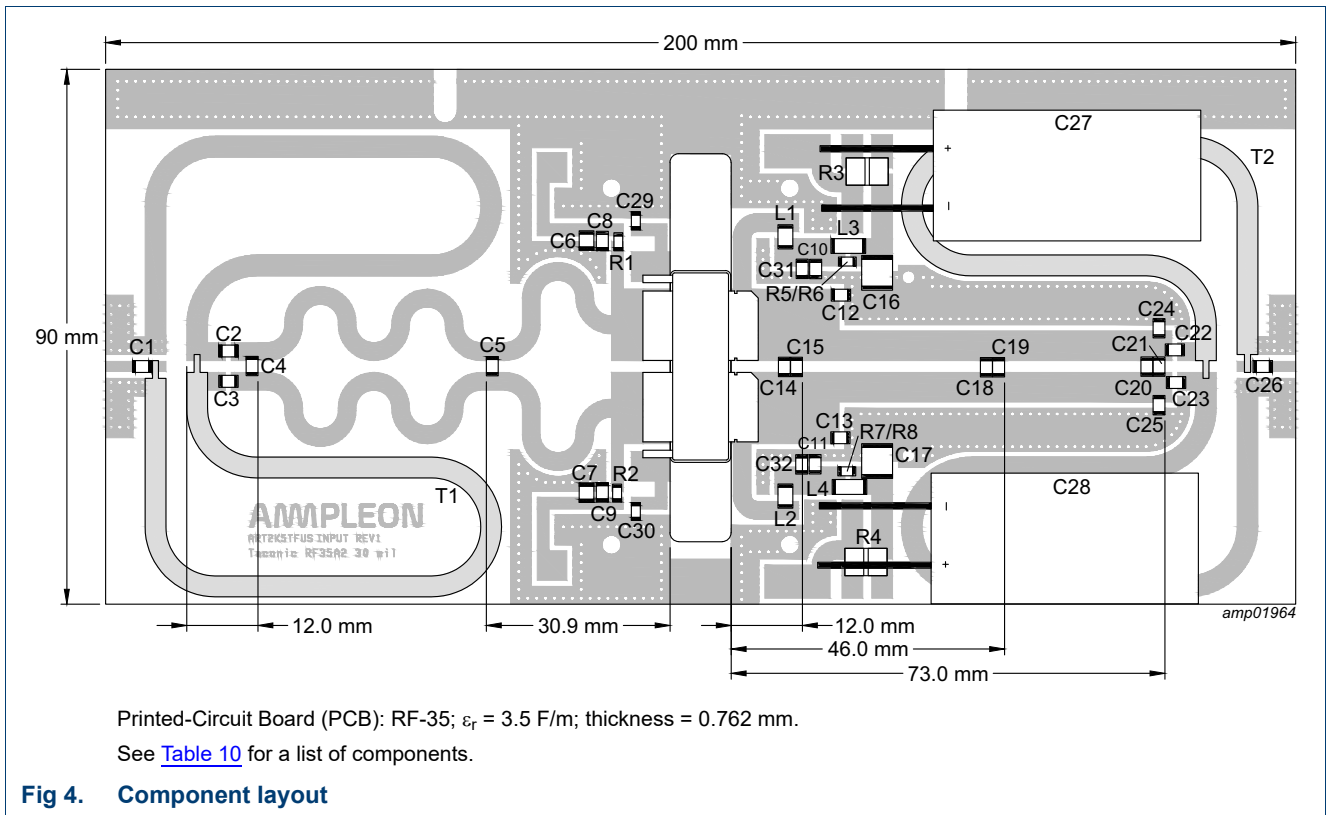


Table 10. List of components

For test circuit see [Figure 4](#).

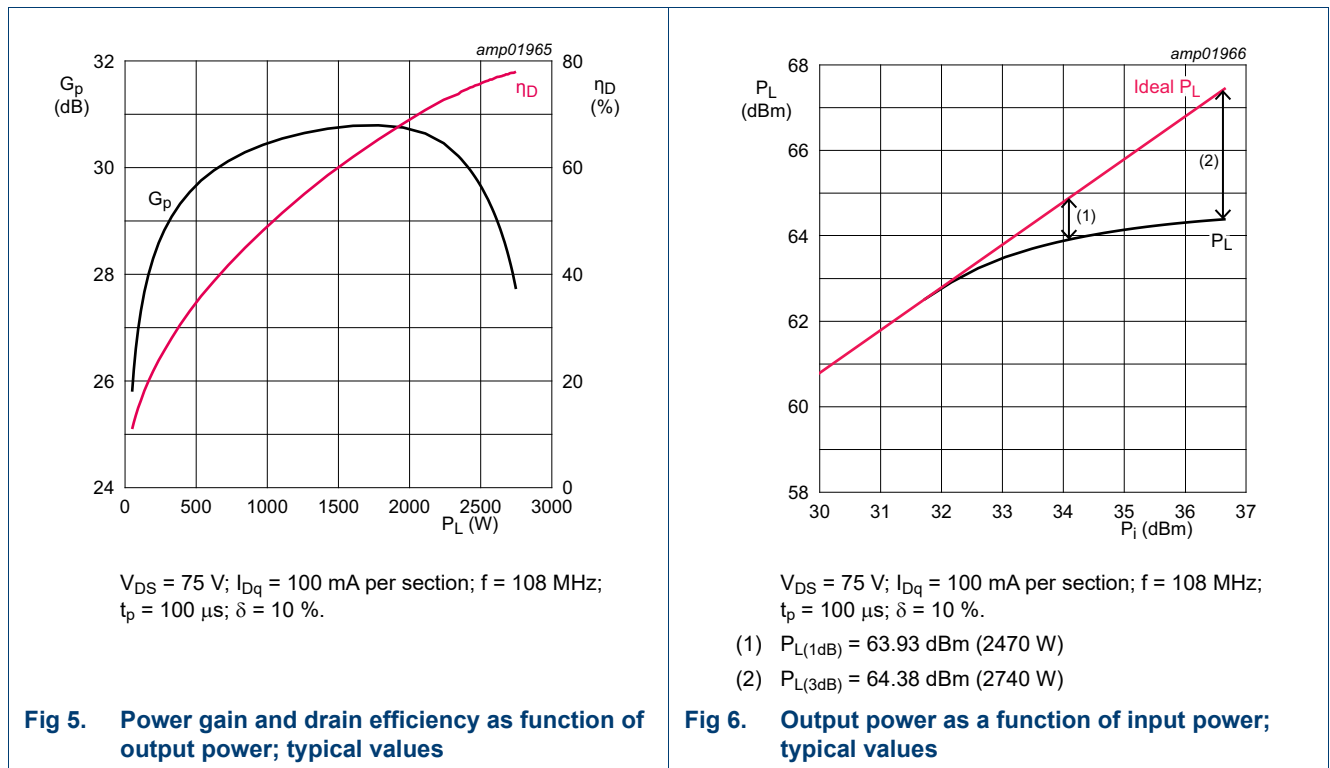
Component	Description	Value	Remarks
C1, C10, C11, C26, C31, C32	multilayer ceramic chip capacitor	470 pF	[1]
C2, C3	multilayer ceramic chip capacitor	68 pF	[1]
C4	multilayer ceramic chip capacitor	43 pF	[1]
C5	multilayer ceramic chip capacitor	300 pF	[1]
C6, C7	multilayer ceramic chip capacitor	4.7 μ F, 50 V	Murata: GRM32ER71H475KA88L
C8, C9	multilayer ceramic chip capacitor	820 pF	[1]
C12, C13	multilayer ceramic chip capacitor	180 pF	[1]
C14, C15	multilayer ceramic chip capacitor	39 pF	[1]
C16, C17	multilayer ceramic chip capacitor	4.7 μ F, 100 V	TDK: C5750X7R2A475KT/A
C18, C19	multilayer ceramic chip capacitor	56 pF	[1]
C20, C21	multilayer ceramic chip capacitor	62 pF	[1]
C22, C23	multilayer ceramic chip capacitor	120 pF	[1]
C24, C25	multilayer ceramic chip capacitor	20 pF	[1]
C27, C28	electrolytic capacitor	2200 μ F, 100 V	
C29, C30	multilayer ceramic chip capacitor	1 μ F, 25 V	SMD 1206
L1, L2	square air core inductor	47 nH	Coilcraft: 1515SQ-47N

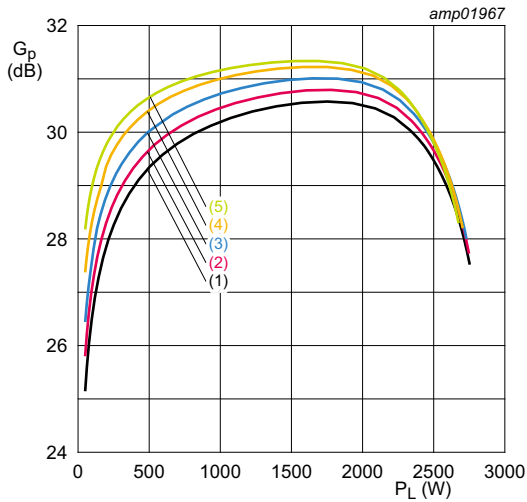
Table 10. List of components ...continued
For test circuit see [Figure 4](#).

Component	Description	Value	Remarks
L3, L4	square air core inductor	82 nH	Coilcraft: 1515SQ-82N
R1, R2	chip resistor	4.7 kΩ	SMD 1206
R3, R4	chip resistor	0.01 Ω	Vishay: WSHP2818
R5, R6, R7, R8	chip resistor	9.1 Ω	SMD 1206
T1, T2	semi rigid coax	50 Ω, 160 mm	EZ141-AL-TP/M17

[1] American Technical Ceramics type 100B or capacitor of same quality.

7.5 Graphical data

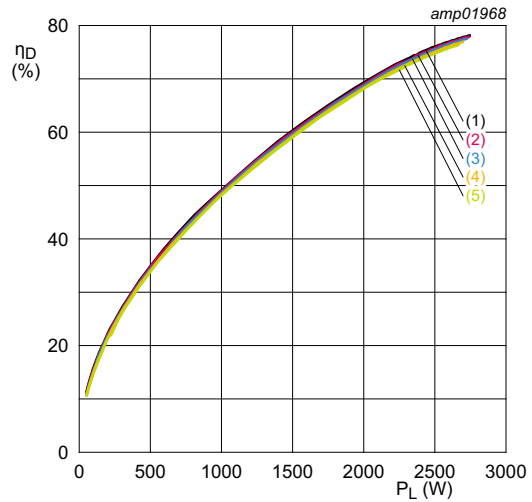




$V_{DS} = 75 \text{ V}$; $f = 108 \text{ MHz}$; $t_p = 100 \text{ }\mu\text{s}$; $\delta = 10 \text{ \%}$.

- (1) $I_{Dq} = 50 \text{ mA}$ per section
- (2) $I_{Dq} = 100 \text{ mA}$ per section
- (3) $I_{Dq} = 200 \text{ mA}$ per section
- (4) $I_{Dq} = 400 \text{ mA}$ per section
- (5) $I_{Dq} = 600 \text{ mA}$ per section

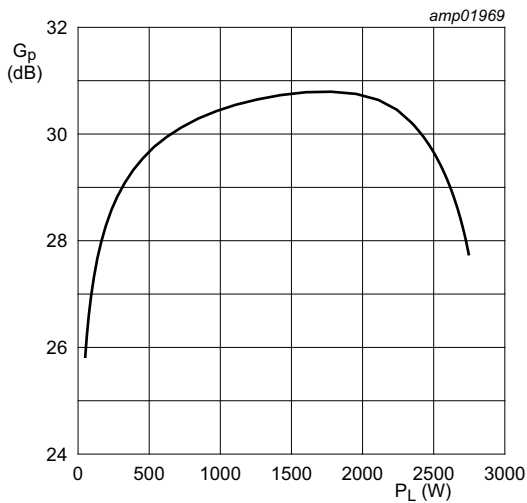
Fig 7. Power gain as a function of output power; typical values



$V_{DS} = 75 \text{ V}$; $f = 108 \text{ MHz}$; $t_p = 100 \text{ }\mu\text{s}$; $\delta = 10 \text{ \%}$.

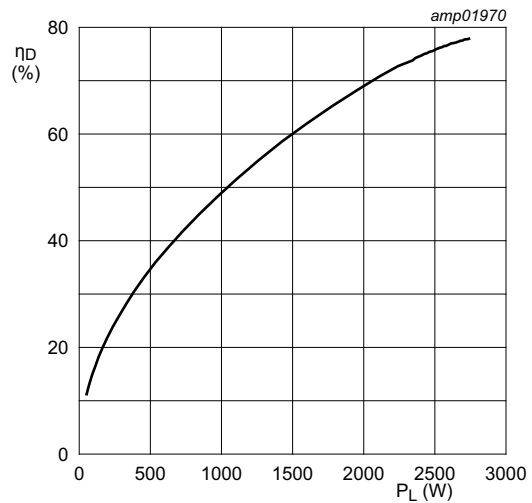
- (1) $I_{Dq} = 50 \text{ mA}$ per section
- (2) $I_{Dq} = 100 \text{ mA}$ per section
- (3) $I_{Dq} = 200 \text{ mA}$ per section
- (4) $I_{Dq} = 400 \text{ mA}$ per section
- (5) $I_{Dq} = 600 \text{ mA}$ per section

Fig 8. Drain efficiency as a function of output power; typical values



$V_{DS} = 75 \text{ V}$; $I_{Dq} = 100 \text{ mA}$ per section; $f = 108 \text{ MHz}$; $t_p = 100 \text{ }\mu\text{s}$; $\delta = 10 \text{ \%}$.

Fig 9. Power gain as a function of output power; typical values



$V_{DS} = 75 \text{ V}$; $I_{Dq} = 100 \text{ mA}$ per section; $f = 108 \text{ MHz}$; $t_p = 100 \text{ }\mu\text{s}$; $\delta = 10 \text{ \%}$.

Fig 10. Drain efficiency as a function of output power; typical values

8. Package outline

Earless flanged ceramic package; 6 leads

ACC-1230-6F-2

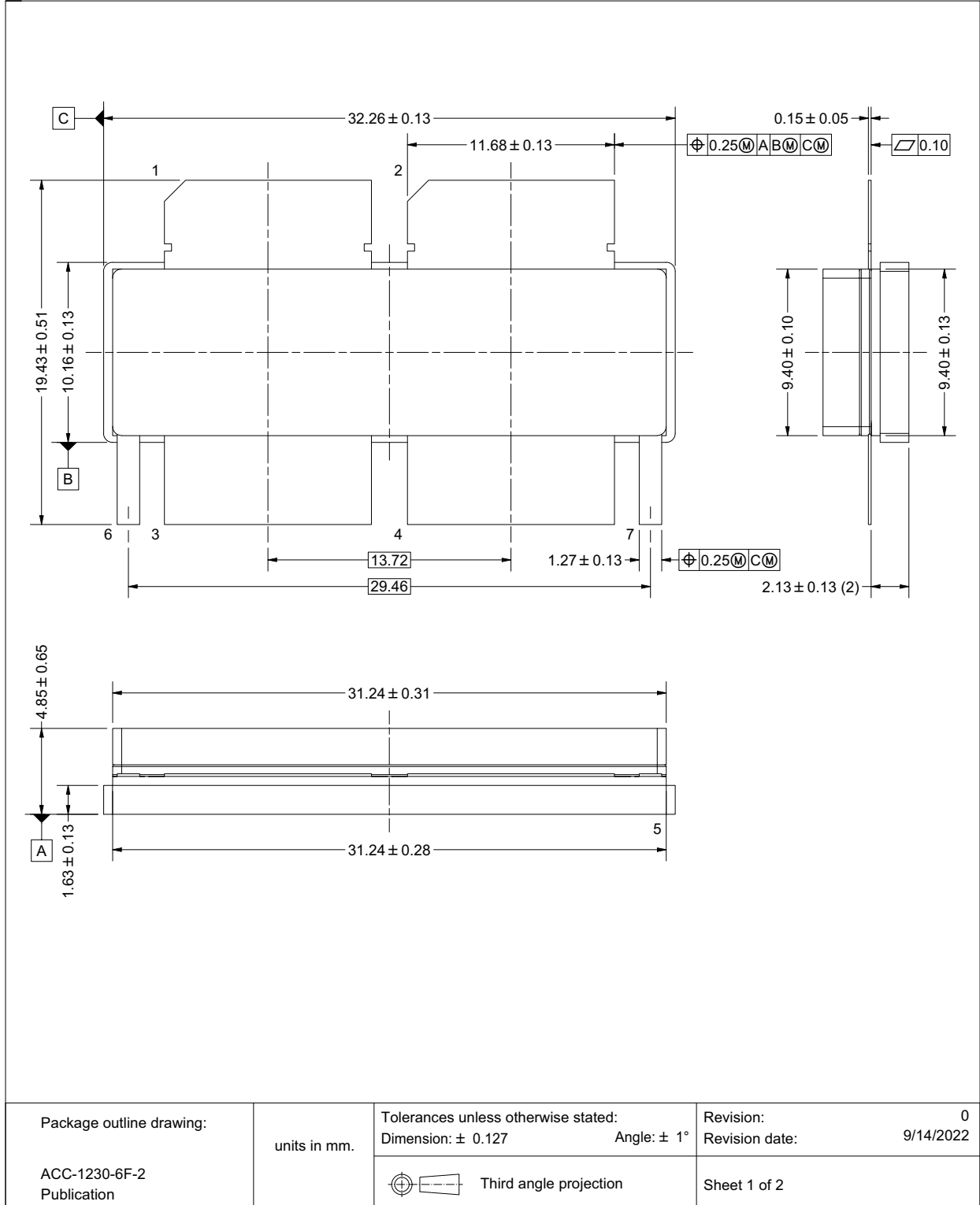


Fig 11. Package outline ACC-1230-6F-2 (sheet 1 of 2)

Earless flanged ceramic package; 6 leads

ACC-1230-6G-2

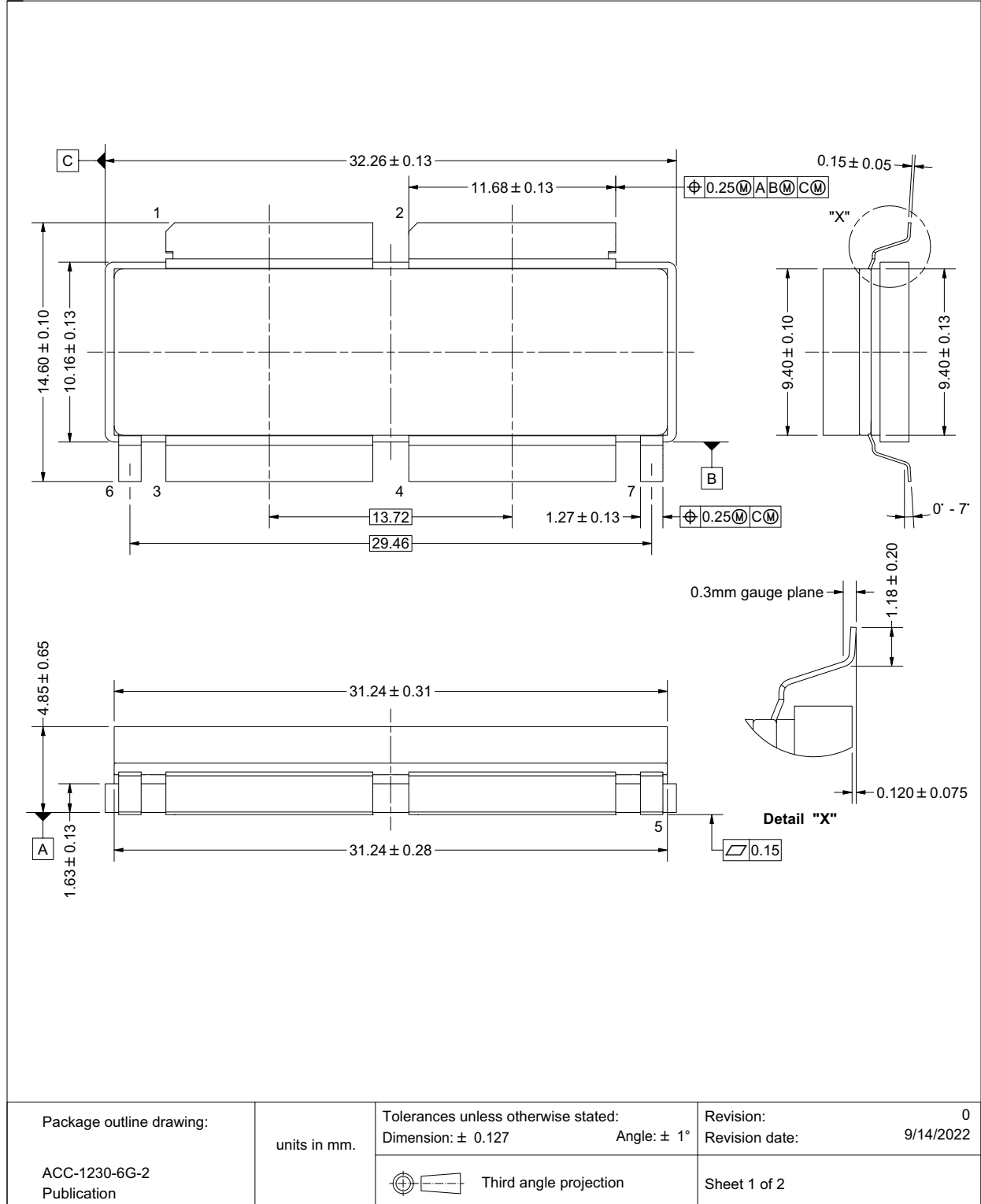


Fig 13. Package outline ACC-1230-6G-2 (sheet 1 of 2)

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or equivalent standards.

Table 11. ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C2A [1]
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	2 [2]

[1] CDM classification C2A is granted to any part that passes after exposure to an ESD pulse of 500 V.

[2] HBM classification 2 is granted to any part that passes after exposure to an ESD pulse of 2000 V.

10. Abbreviations

Table 12. Abbreviations

Acronym	Description
CW	Continuous Wave
ESD	ElectroStatic Discharge
FM	Frequency Modulation
FET	Field-Effect Transistor
ISM	Industrial, Scientific and Medical
LDMOS	Laterally Diffused Metal-Oxide Semiconductor
MRI	Magnetic Resonance Imaging
MTF	Median Time to Failure
RoHS	Restriction of Hazardous Substances
UHF	Ultra High Frequency
VHF	Very High Frequency
VSWR	Voltage Standing Wave Ratio

11. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
ART2K5TFUS_ART2K5TFUG v.1	20240208	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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