# AR201070

ART2K0FE, 88-108MHz

v1.1 — 8-Sept-2021



Document info	Document information						
Status Company Public							
Author(s)	Ampleon						
Abstract	Measurement results of a Class AB design for the 88-108MHz band with the ART2K0FE						

AR201070

ART2K0FE 88-108MHz

# 1. Revision History

Table 1: Report revisions

Revision	Date	Description	Author
1.0	8-Sept-2021	Initial document	Harrie Rahangmetan
1.1	6-Dec-2021	Value C23 corrected	Harrie Rahangmetan

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### 5. Introduction

#### 5.1 General description

This report presents the measurement results of the Class AB amplifier demo AR201070. The device ART2K0FE used is a 2000 W advanced ruggedness LDMOS power transistor for industrial, scientific and medical applications in the HF to 400 MHz band, 9th generation LDMOS in a SOT539 package. ART2K0FE is a symmetrical push-pull power transistor. The presented demo is tuned for the frequency band 88-108MHz.

#### 5.2 Test object details

Transistor type: ART2K0FE (Soldered down)

Production code: m2108-1200 Package: SOT539

Board: ART2K0FE\_input\_output\_rev4.3

Demo number: AR201070

#### 5.3 Used Test signals

CW: CW (Vds=58V - 62V)

#### 5.4 Test circuit

A description of this circuit can be found in Appendix A.

Start with a supply voltage (drain-source) of 60V. The total Idq should be 100mA (2x50mA).

Start with Vgs1=1.5V and increase until Idq1=50mA.

Then Vgs2=1.5V and increase until Idq2=50mA.

Leave the Vgs as it is, and you can vary Vds from 58V till 62V.

#### 6. Measurement Results

#### 6.1 Summary CW Power Sweeps (Vds=58V, results @ 1500W)

Freq [MHz]	MaxGain [dB]	MaxEff [%]	G@MxEff [dB]	P1dB [dBm]*	P1dB [W]*	G@P1dB [dB]*	Eff@P1dB [%]*	P3dB [dBm]*	P3dB [W]*	G@P3dB [dB]*	Eff@P3dB [%]*
88.00	28.1	85.4	24.9	61.3	1346.01	27.1	81.5	61.8	1514.71	25.1	85.3
93.00	28.2	84.2	25.0	61.1	1278.43	27.2	77.5	61.9	1566.19	25.2	83.9
98.00	28.1	82.8	25.1	61.1	1275.49	27.1	74.9	62.1	1633.55	25.1	82.7
103.00	27.8	82.2	24.7	61.2	1306.15	26.8	73.7	62.3	1700.79	24.8	82.0
108.00	27.7	81.9	24.6	61.3	1363.69	26.7	73.8	62.4	1735.64	24.7	81.8

Freq [MHz]	Gain [dB] @ 1500W	Eff [%] @ 1500W	Compr [dB] @ 1500W	IRL [dB] @ 1500W
88.00	25.4	85.0	-2.65	11.8
93.00	26.0	82.6	-2.22	14.3
98.00	26.2	80.0	-1.91	17.5
103.00	26.2	78.0	-1.68	22.2
108.00	26.2	76.9	-1.47	24.5

#### 6.2 Gain & Efficiency @ Frequency=88-108MHz CW, Vds=58V

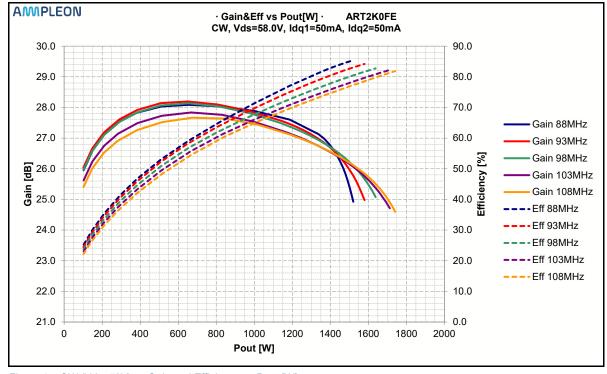


Figure 1 CW (Vds=58V) Gain and Efficiency vs Pout [W]

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### 6.3 Summary CW Power Sweeps (Vds=60V, results @ 1600W)

Freq [MHz]	MaxGain [dB]	MaxEff [%]	G@MxEff [dB]	P1dB [dBm]*	P1dB [W]*	G@P1dB [dB]*	Eff@P1dB [%]*	P3dB [dBm]*	P3dB [W]*	G@P3dB [dB]*	Eff@P3dB [%]*
88.00	28.2	84.8	25.0	61.6	1441.71	27.2	81.3	62.1	1615.68	25.2	84.6
93.00	28.3	84.2	25.3	61.3	1362.09	27.3	77.4	62.2	1678.66	25.3	84.2
98.00	28.2	83.0	25.1	61.3	1349.69	27.2	74.8	62.4	1742.56	25.2	82.9
103.00	27.9	82.4	24.8	61.4	1377.33	26.9	73.4	62.6	1807.65	24.9	82.1
108.00	27.7	81.9	24.6	61.6	1434.42	26.7	73.5	62.7	1844.60	24.7	81.7

Freq [MHz]	Gain [dB] @ 1600W	Eff [%] @ 1600W	Compr [dB] @ 1600W	IRL [dB] @ 1600W
88.00	25.5	84.3	-2.67	11.7
93.00	26.1	82.7	-2.16	14.2
98.00	26.3	80.2	-1.93	17.4
103.00	26.2	78.1	-1.73	22.1
108.00	26.2	77.0	-1.52	24.5

### 6.4 Gain & Efficiency @ Frequency=88-108MHz CW Vds=60V

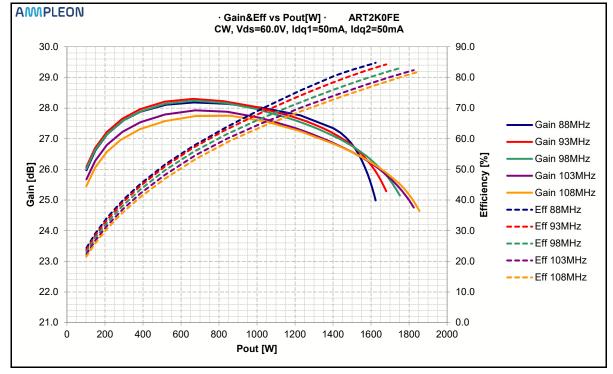


Figure 2 CW (Vds=60V) Gain and Efficiency vs Pout [W]

### 6.5 Second & third harmonic @ Frequency=88-108MHz CW Vds=60V

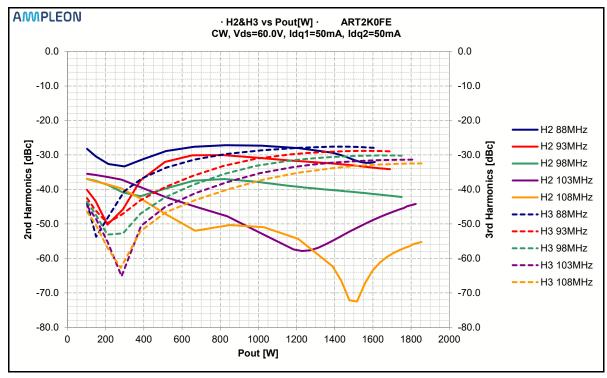


Figure 3 CW (Vds=60V) H2 & H3 vs Pout [W]

#### 6.6 Summary CW Power Sweeps (Vds=62V, results @ 1650W)

Freq [MHz]	MaxGain [dB]	MaxEff [%]	G@MxEff [dB]	P1dB [dBm]*	P1dB [W]*	G@P1dB [dB]*	Eff@P1dB [%]*	P3dB [dBm]*	P3dB [W]*	G@P3dB [dB]*	Eff@P3dB [%]*
88.00	28.2	82.4	25.1	61.7	1487.29	27.2	79.1	62.2	1672.66	25.2	82.3
93.00	28.3	83.4	25.3	61.5	1425.28	27.3	76.2	62.5	1778.39	25.3	83.4
98.00	28.2	81.8	25.2	61.5	1402.39	27.2	73.2	62.7	1844.27	25.2	81.8
103.00	27.9	81.1	24.8	61.6	1434.90	26.9	71.9	62.8	1910.10	24.9	80.9
108.00	27.7	80.5	24.7	61.7	1483.18	26.7	71.6	62.9	1941.63	24.7	80.3

Freq [MHz]	Gain [dB] @ 1650W	Eff [%] @ 1650W	Compr [dB] @ 1650W	IRL [dB] @ 1650W
88.00	25.7	81.9	-2.55	11.6
93.00	26.4	81.0	-1.88	14.0
98.00	26.5	78.3	-1.78	17.2
103.00	26.3	76.2	-1.62	21.9
108.00	26.2	74.9	-1.48	24.5

### 6.7 Gain & Efficiency @ Frequency=88-108MHz CW Vds=62V

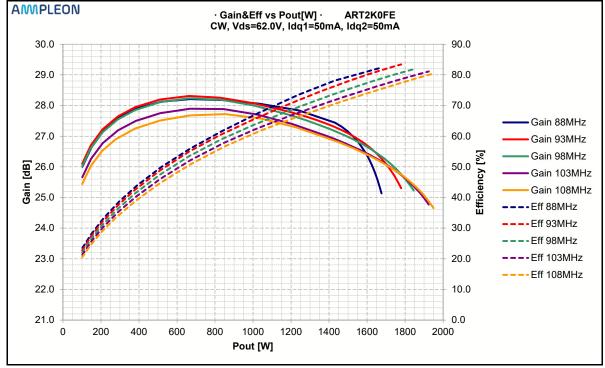
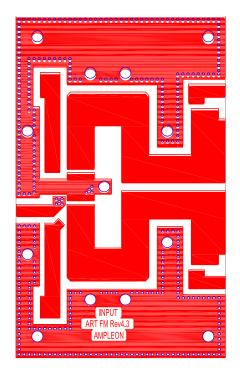


Figure 4 CW (Vds=62V) Gain and Efficiency vs Pout [W]

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### 7. Appendix A

## 7.1 PCB Layout Drawing top and bottom



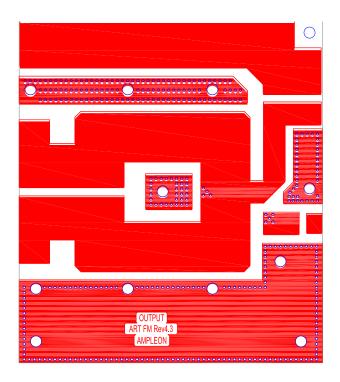
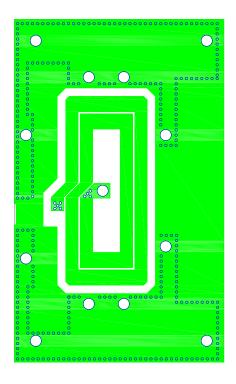


Figure 5 PCB layout top layer



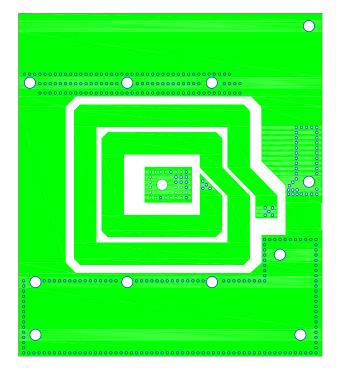


Figure 6 PCB layout

bottom layer

#### 7.2 PCB Layout Drawing + Components

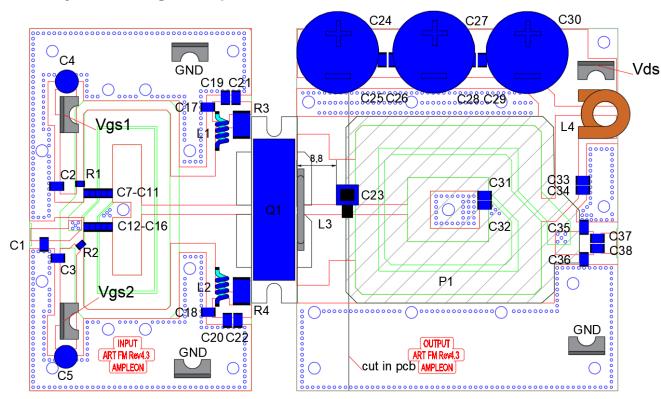


Figure 7 PCB Layout Drawing + Components

#### Note:

The output pcb is cut in two. The part close to the drain of the transistor and the transistor itself is soldered to the base plate. The other part of the pcb at the output connector side is screwed down. The input pcb is screwed down to the base plate. The cavity in the base plate under the input pcb is not filled. Air only. The cavity in the base plate under the output pcb is filled with Thermipad (blue).

See section 8.6 Building Sequence Demo board.

#### 7.3 L3 dimensions

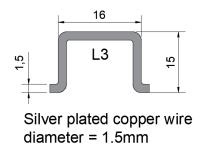


Figure 8 L3 Dimensions



### 7.4 Component list

Table 1: Component list

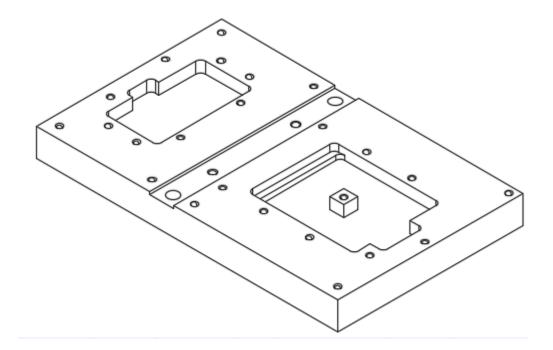
Designator	Description	Manufacturer	Part #			
C1	18pF	ATC	100B			
C2, C3, C17, C18	10nF	AVX	12101C1033KAT2A			
C4, C5	22uF electrolytic capacitor					
C7-C11, C12-C16	4.7nF	Murata	GRM2165C1H472JA01D			
C19, C20	1nF	ATC	100B			
C21, C22	470pF	ATC	100B			
C23	68pF	CDE	MIN-002			
C24, C27, C30	1000uF electrolytic capacitor	PHILIPS				
C25, C26, C28, C29	910pF	ATC	100B			
C31, C32, C33, C34	1nF	PPI	1111N			
C35, C36	11pF	ATC	100B			
C37, C38	470pF	ATC	100B			
R1, R2	10 Ohm		1206			
R2, R3	33 Ohm, 2W	TE CONN	CRGP2512F33R			
L1, L2	17.5nH	COILCRAFT	B06TGLC			
L3	Figure 16, silver plated wire		1.5mm diameter			
L4	22nH	COILCRAFT	1212VS-22NM EB			
Q1	ART2K0FE	AMPLEON				
Base plate	Copper with water cooling channel		Cavity for coplanar baluns are 5mm deep			
P1	Thermal conductor under the output balun in the cavity of the base plate	Mueller Ahlhorn	Thermipad TP22626 Er=6.7			
PCB Material: Arlon TC350, thickness 0.762 mm (30 mil), Er = 3.5, Cu = 2x70 micron						

#### 7.5 Baseplate

Please note that this drawing is the standard base plate. This baseplate for the demo AR201070 needed some rework so this drawing is just for illustration.

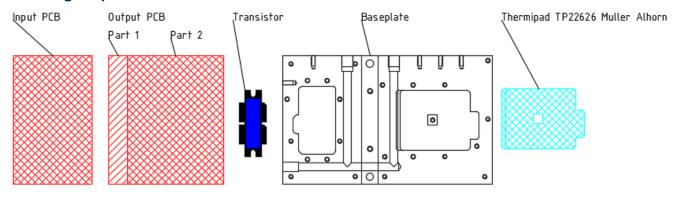
The demo amplifier pcb boards are mounted on a full copper base plate. The base plate contains a water channel to supply the amplifier with enough cooling.

The base plate contains two cavities for the coplanar baluns. The input balun cavity is air filled. The output balun cavity is filled with a thermal conductive material that has good electrical properties. The material is conducting the heat from the balun, generated because of RF losses, to the baseplate. The thermal conductive material is necessary to cool the coplanar output balun.



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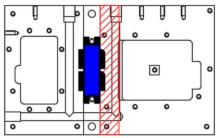
# 7.6 Building sequence Demo board



Transistor and part 1 output PCB soldered on the base plate





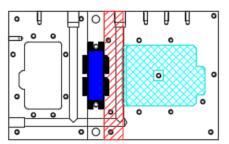




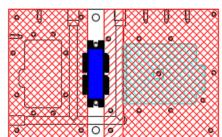
Thermipad put in the base plate cavity







Input PCB and Part 2 of output PCB screwed down to the base plate



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### 8. Photo's Demo Board

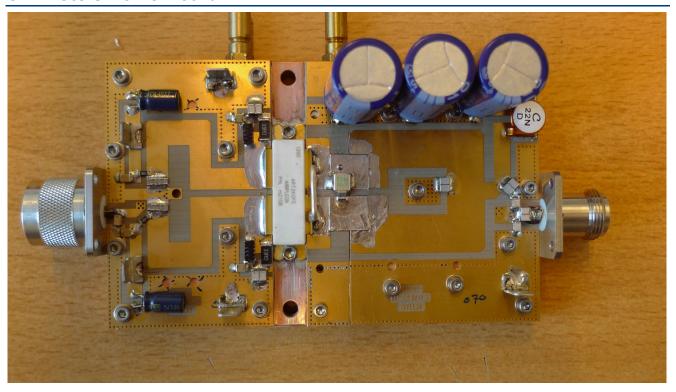


Figure 9 Picture Top View Demo Board

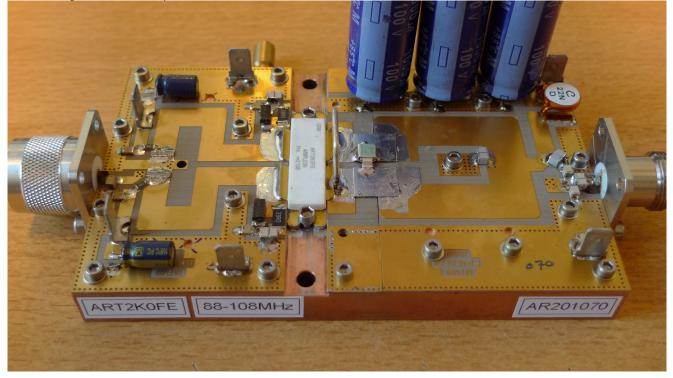


Figure 10 Picture Side View Demo Board

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