

# AR192035

BLP15M9S30, 30-520 MHz

V1.0 — 31 October 2019

**AMPLEON**

Application Report

## Document information

Info	Content
Status	Company Public
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Abstract	Measurement results of the BLP15M9S30 LDMOS Device in Board #AR192035 tuned for the 30-520 MHz band at 28-32V

## 1 Revision History

Table 1. Report revisions

Revision No.	Date	Description	Author
1.0	20190815	Initial document	Bill Goumas

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## 5 General Description

This report presents the measurement results of the Class AB Demo board AR192035 using the BLP15M9S30. The demo achieves ~30W across 30-520MHz.

## 6 Biasing

### 6.1 Bias Details

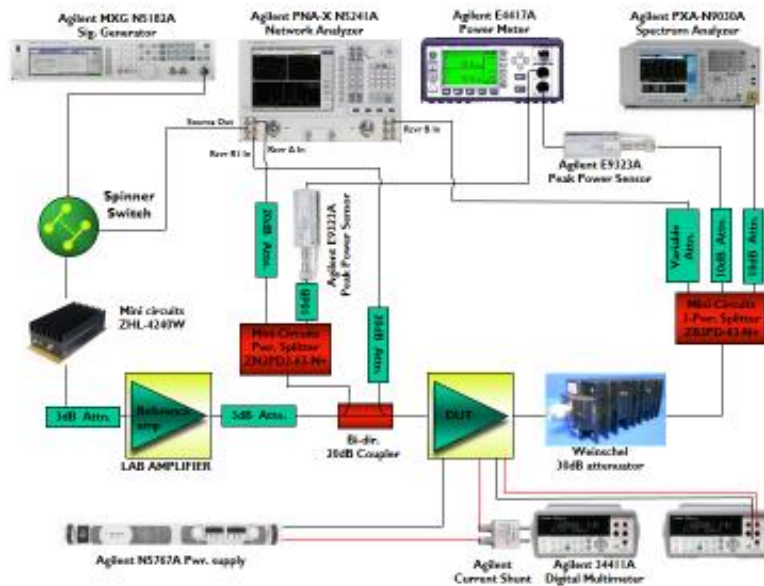
The efficiencies presented include the bias current from the biasing board. The current from the biasing board is ~25mA.

VDD =28

VGS= ~2.1 V, leading to an IDQ =200mA.

## 7 Test Bench Set Up

Figure 1. Test Bench Equipment set up



## 8 Summary

---

Gain is 20-24dB and Efficiency is 45-65% at Pout=30W.

9 Performance Details

9.1 Small Signal Results

Vdd=32V, Idq=200mA Initial Data

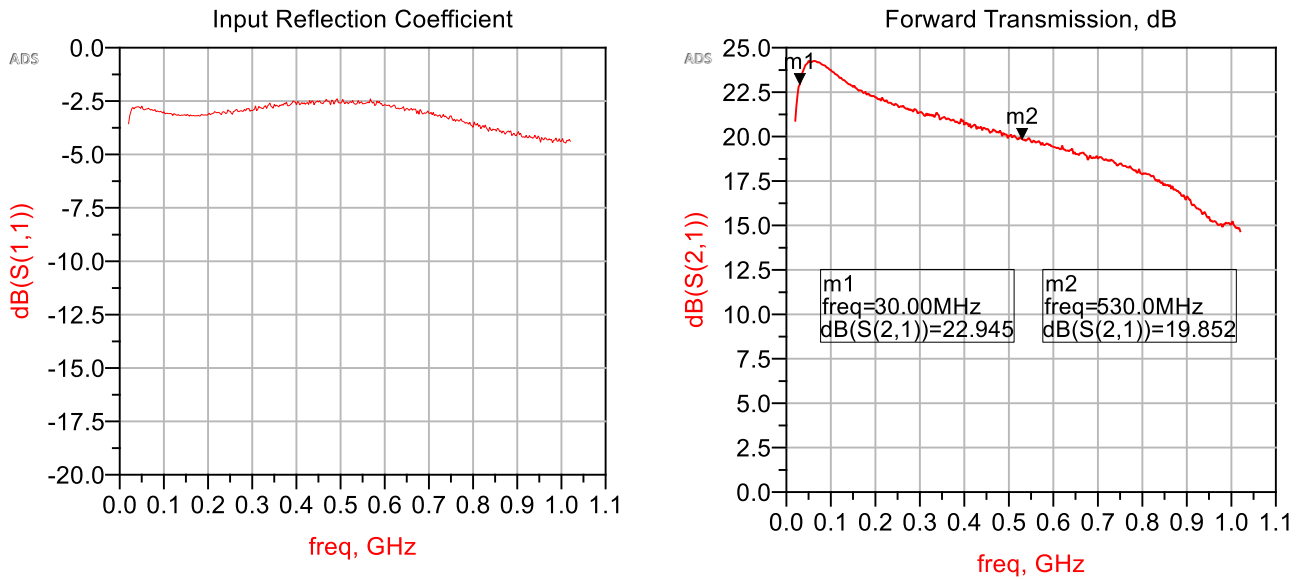
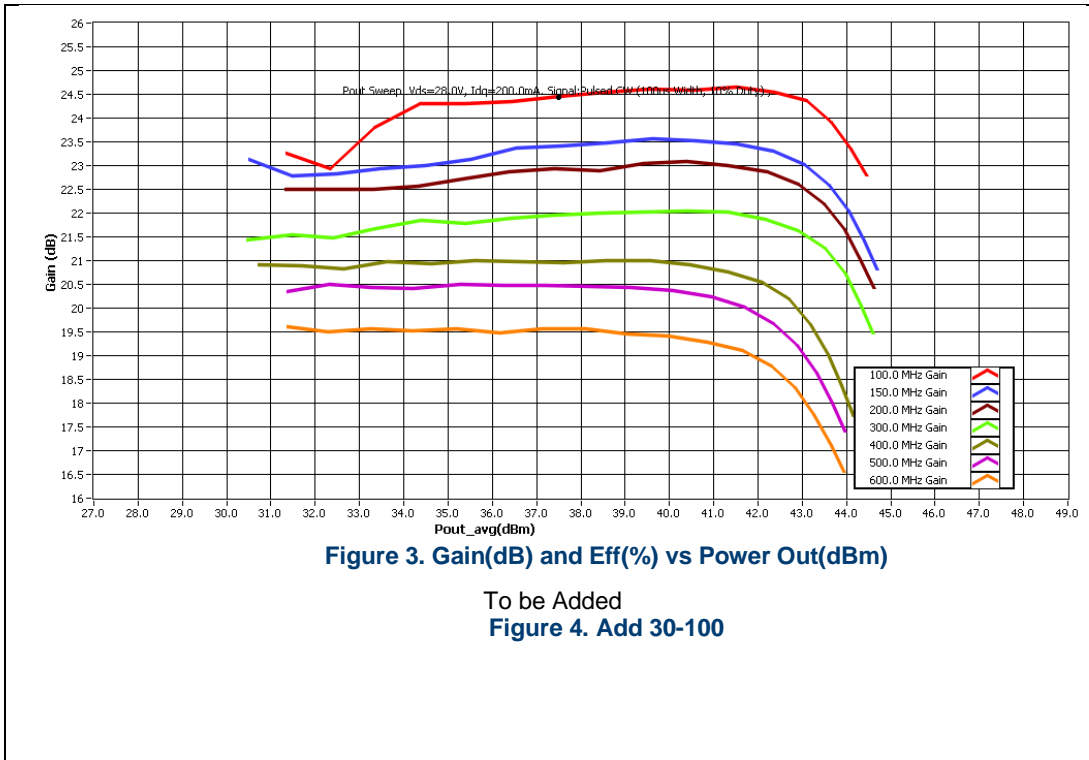


Figure 2. Small Signal Data, Vdd=32V, Idq=200mA, Pin=10dBm

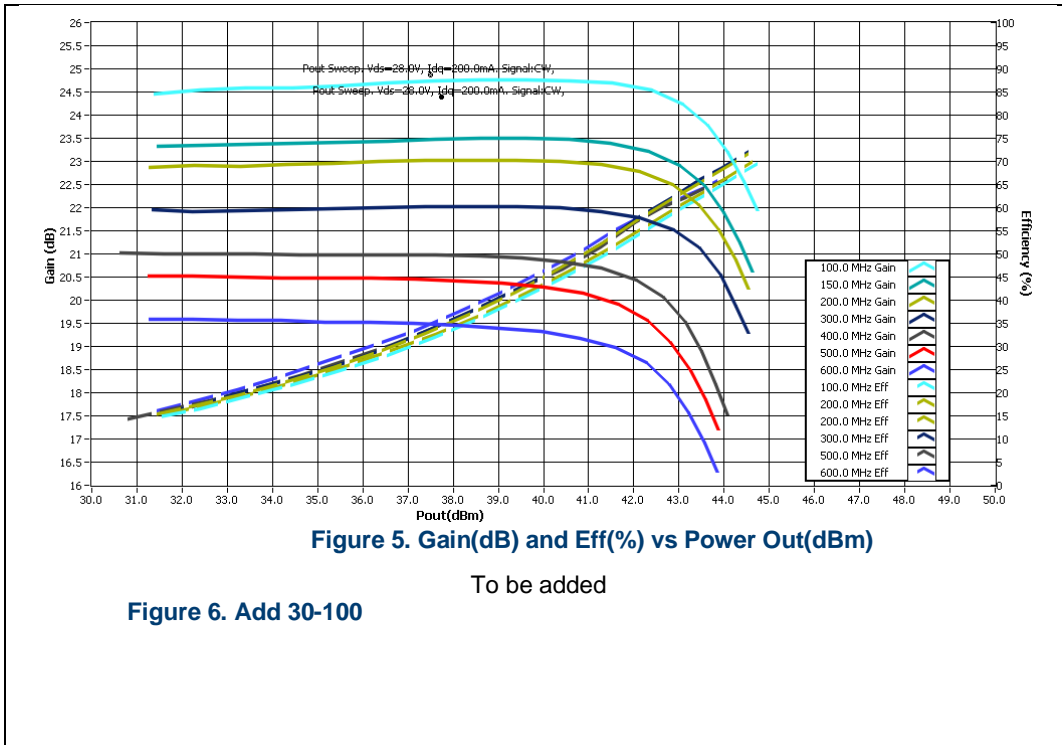
9.2 Pulse Gain and Efficiency Sweeps

Vdd=28V, Idq=200mA, 10% Duty Cycle, 100usec PW



9.3 CW Gain and Efficiency Sweeps

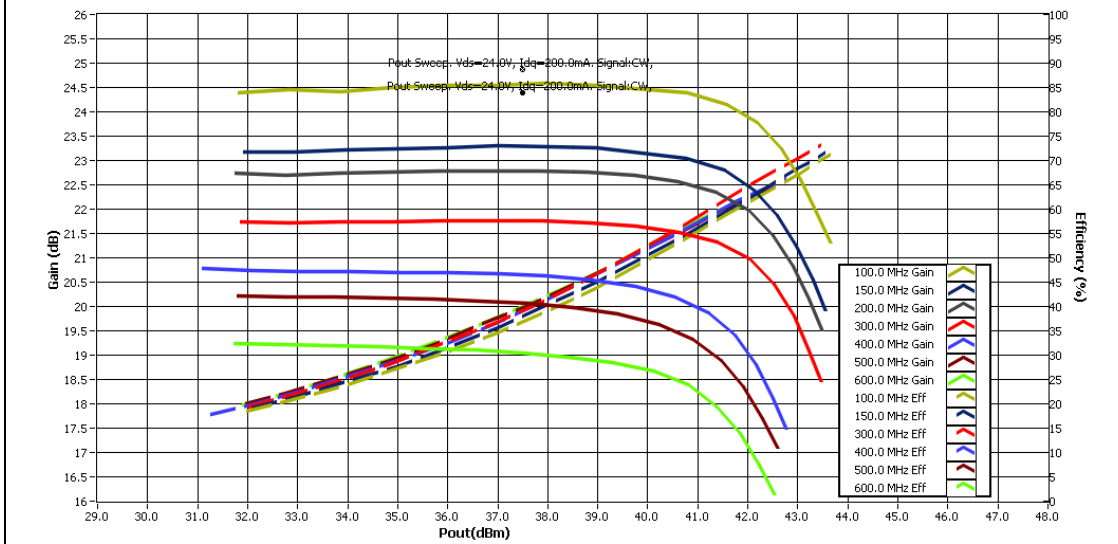
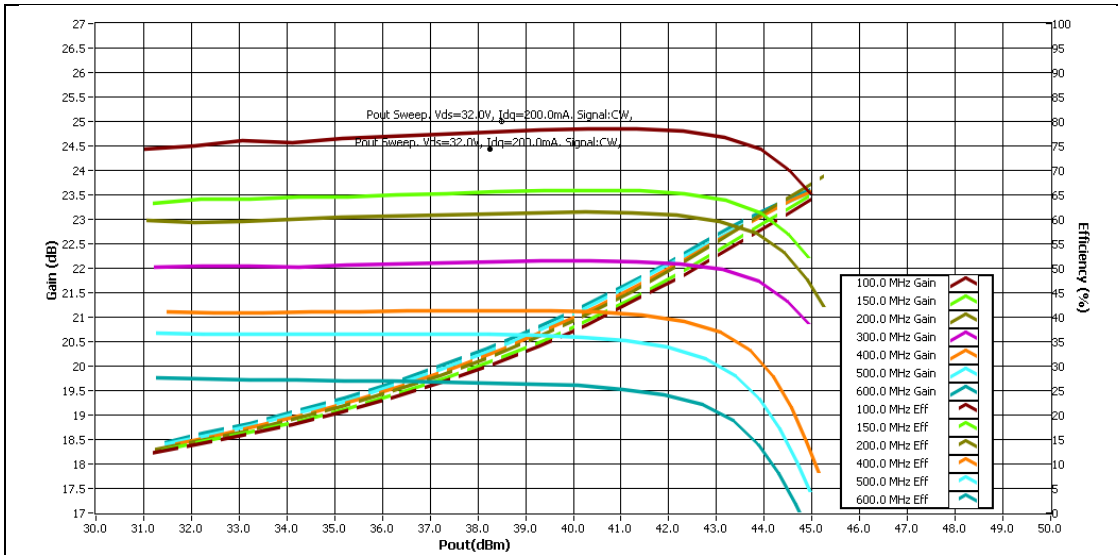
Vdd=28V, Idq=200mA,





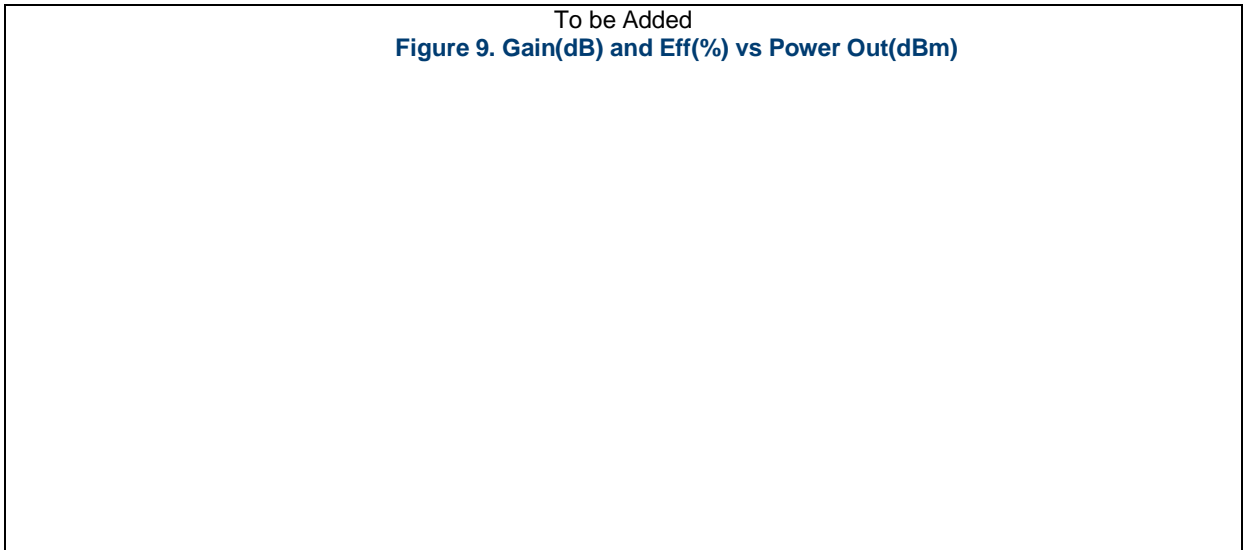
## 9.4 CW Gain and Efficiency Sweeps vs Voltage

Vdd=32V, Idq=200mA,



## 9.5 P1,P3 Comparison

Vdd=50V, , 50% Duty Cycle, 100usec PW,



## 9.6 IMD

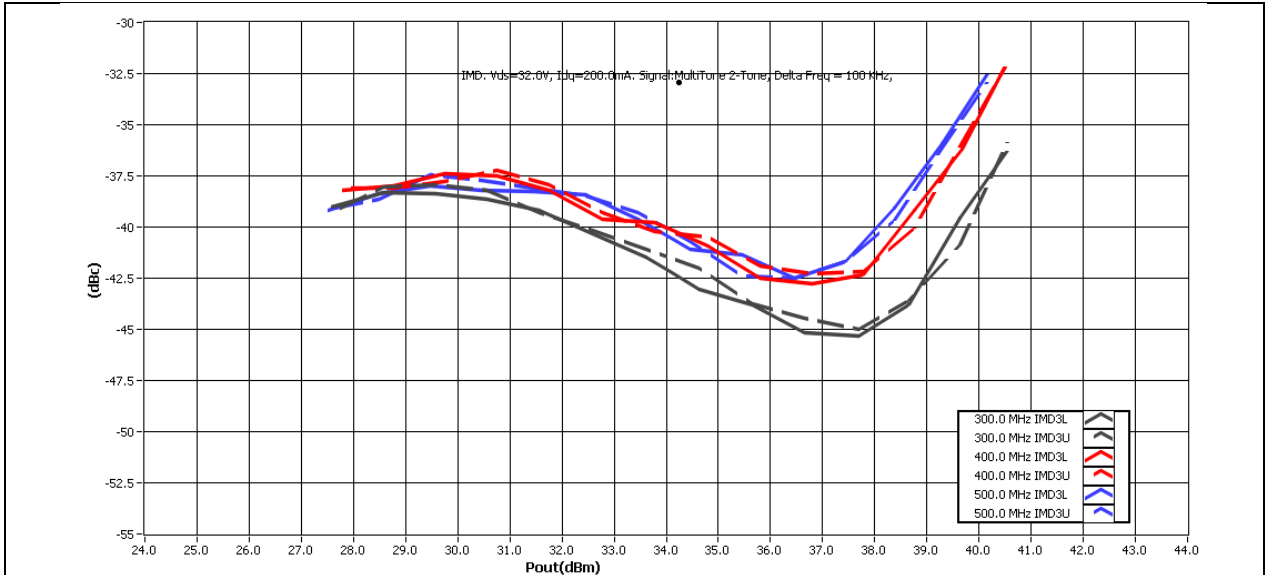


Figure 10. IMD3 vs Frequency(MHz), Vdd=32, Idq=200mA

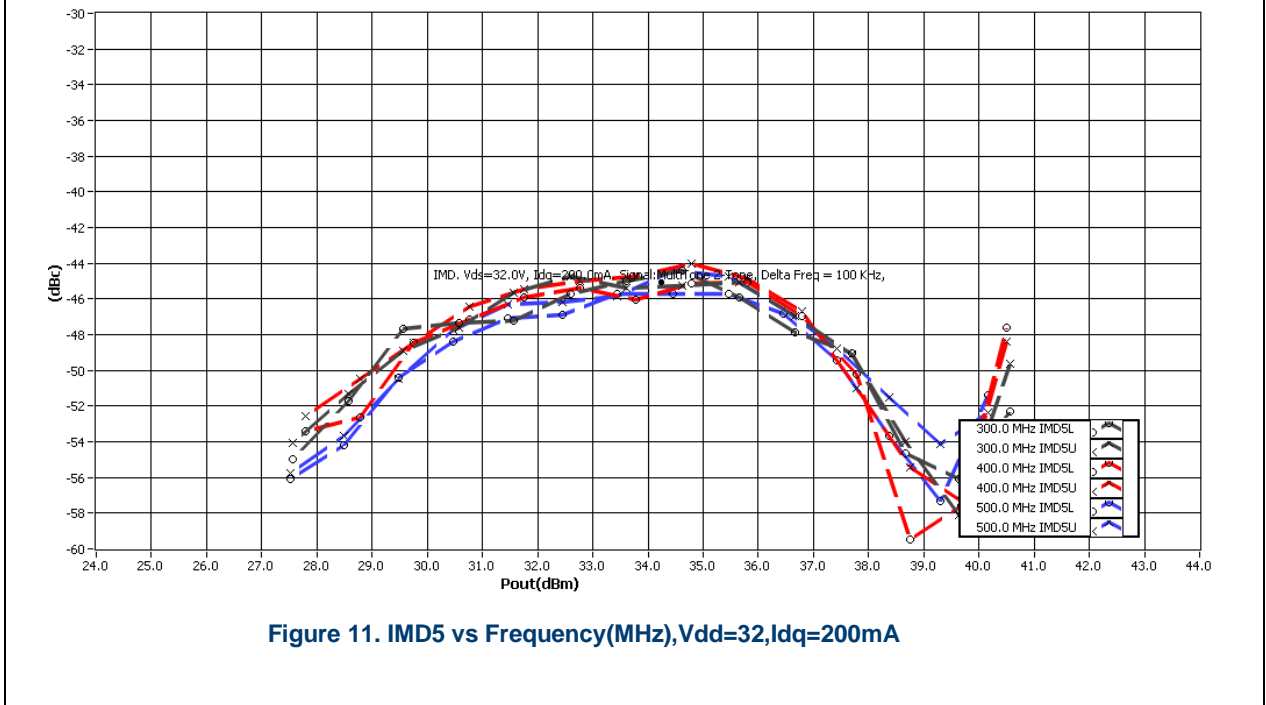


Figure 11. IMD5 vs Frequency(MHz), Vdd=32, Idq=200mA

## 9.7 IMD

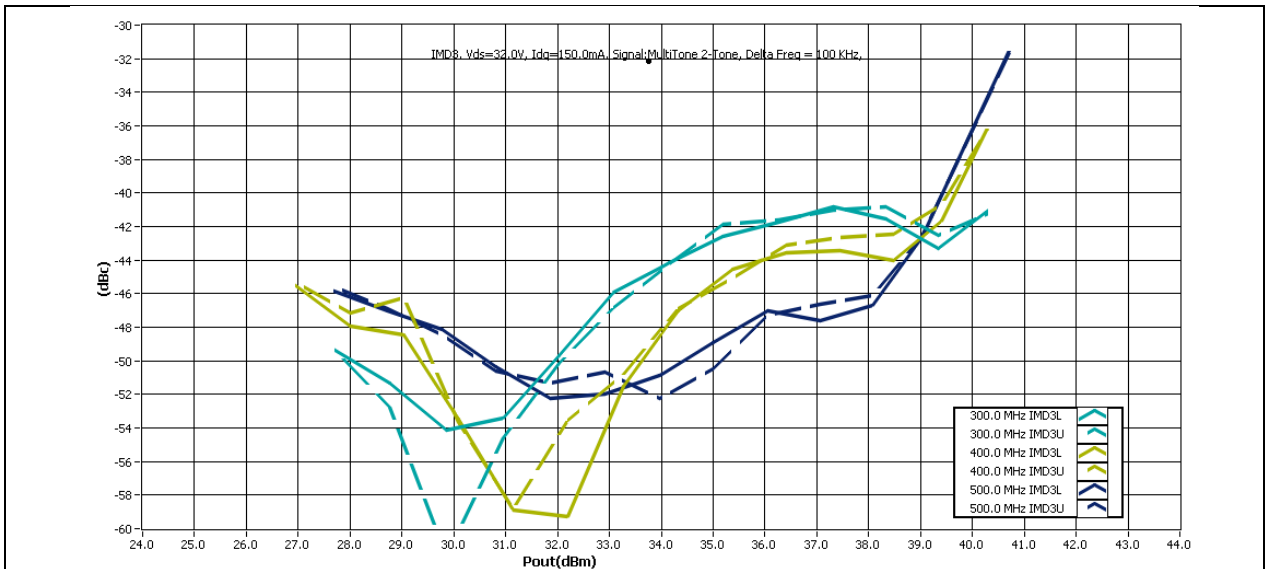


Figure 12. IMD3 vs Frequency(MHz), Vdd=32, Idq=150mA

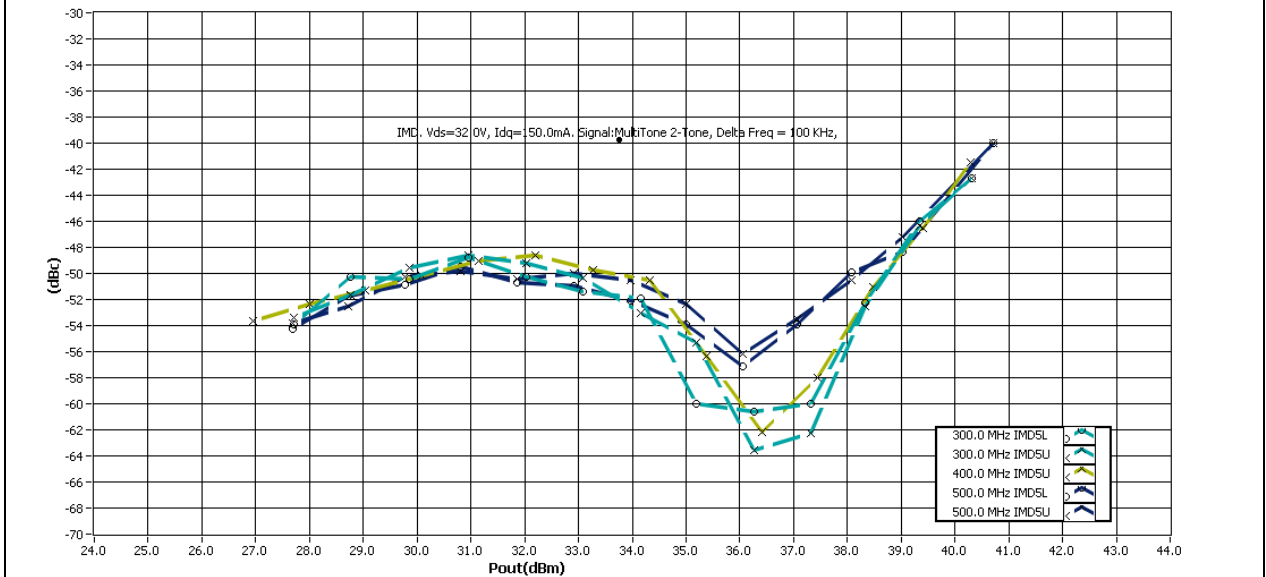


Figure 13. IMD5 vs Frequency(MHz), Vdd=32, Idq=150mA

## 9.8 IMD

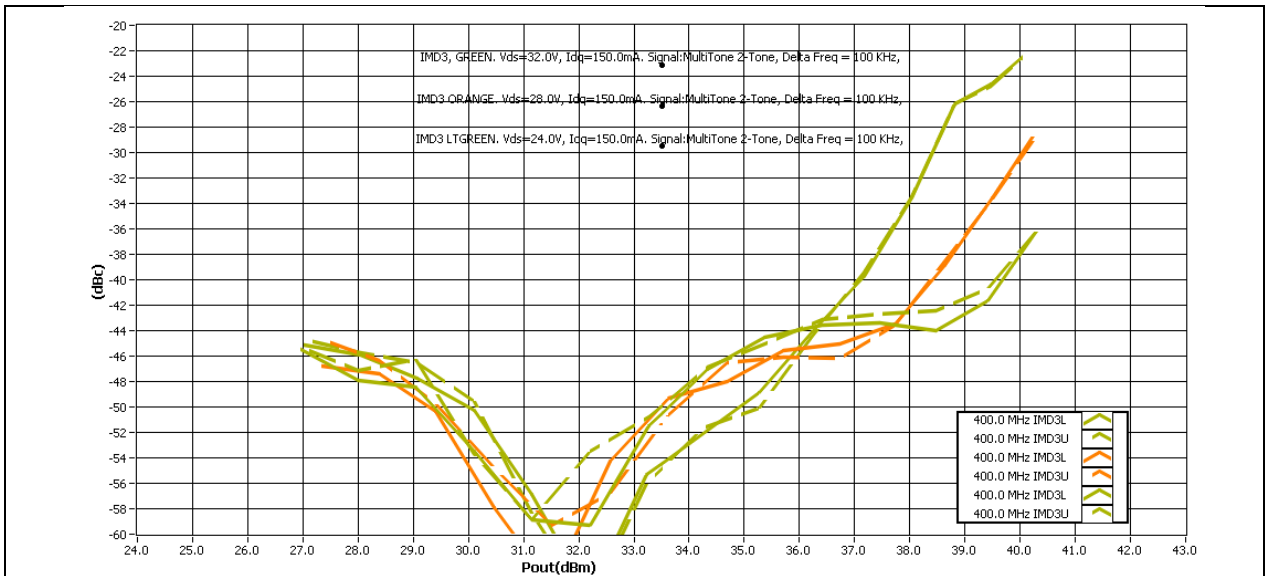


Figure 14. IMD3 vs Voltage, Frequency(=400MHz, Idq=150mA)

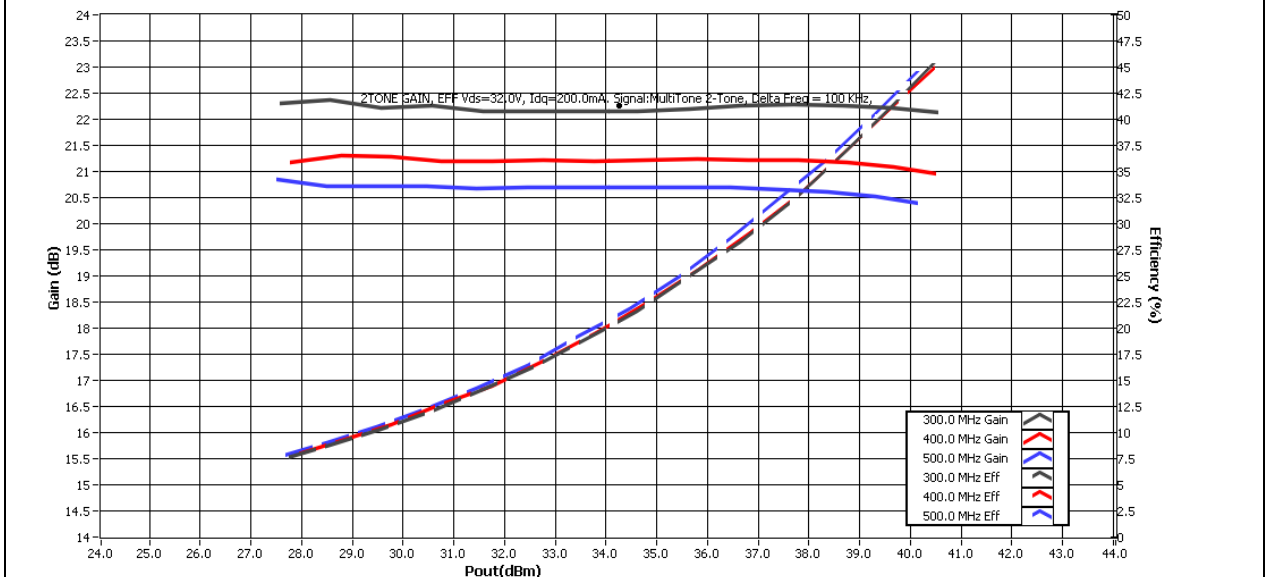
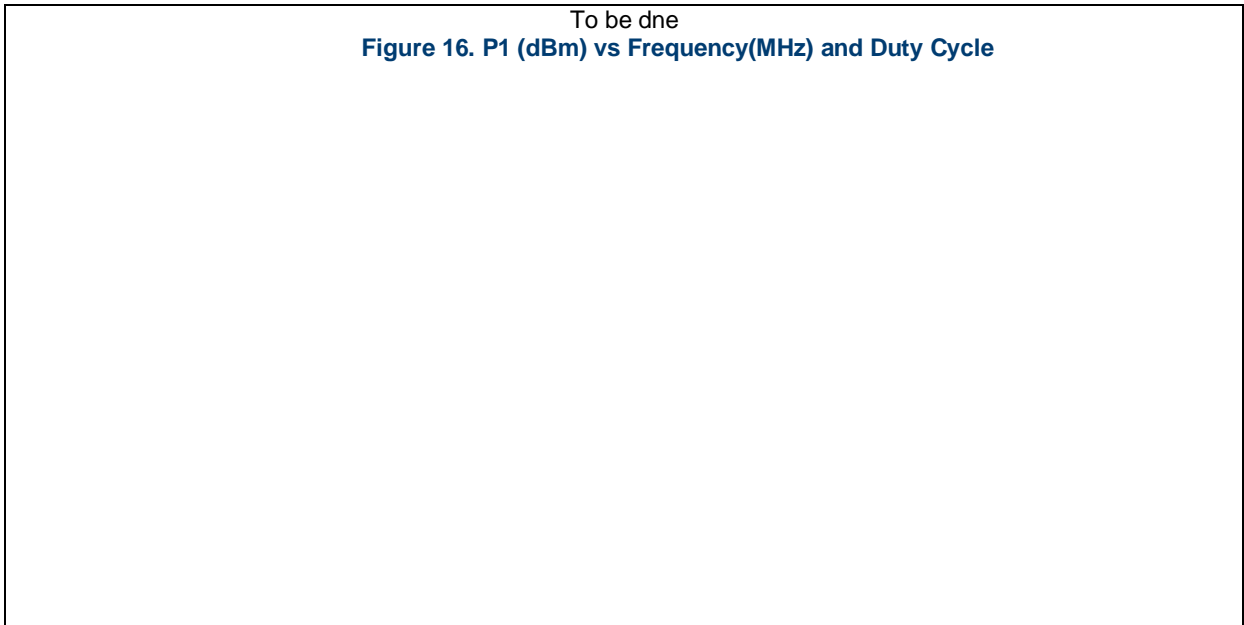


Figure 15. 2Tone Gain, Efficiency Vdd=32, Idq=200mA

## 9.9 P1 versus Duty Cycle and Frequency

Vdd=28V, Idq=200mA, 100usec PW



## 10 Hardware

### 10.1 Board photograph

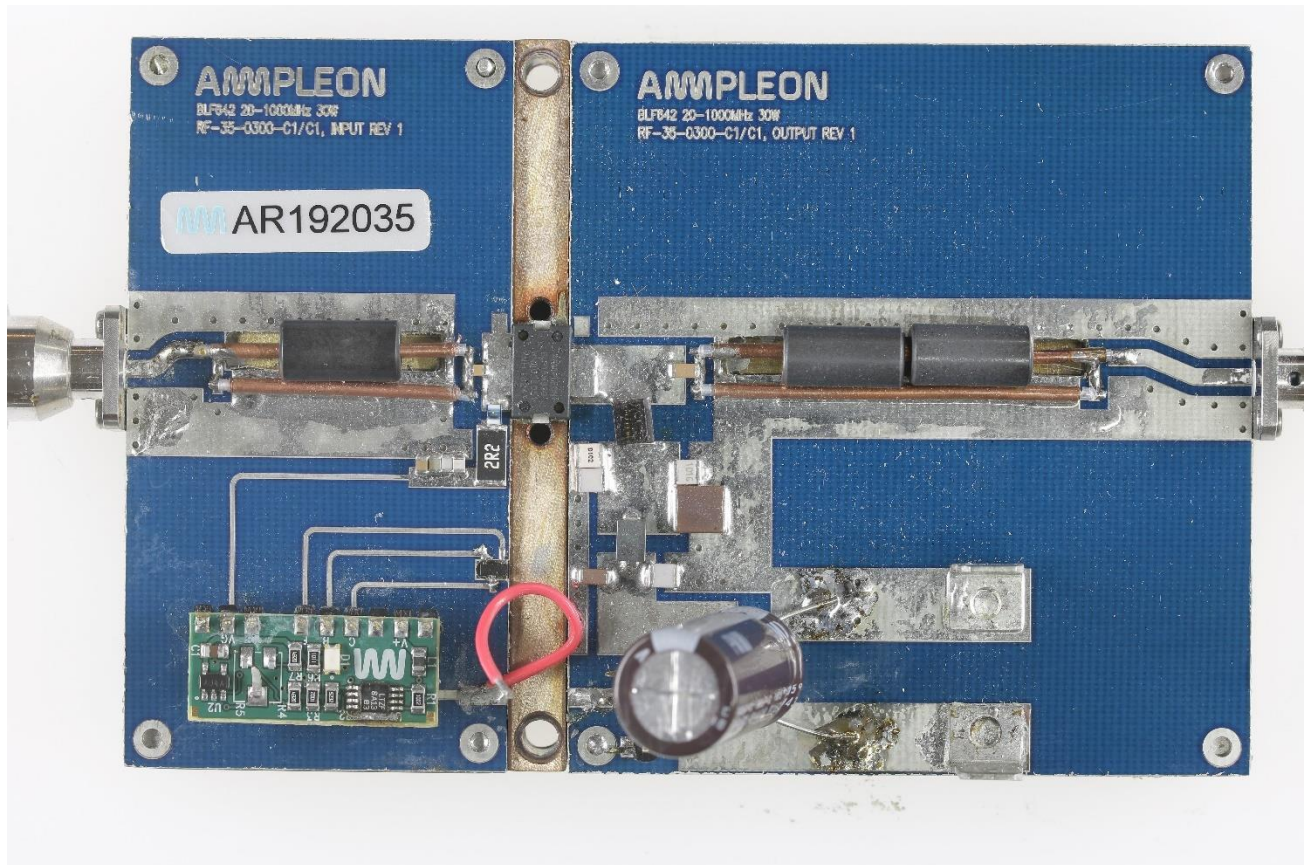


Figure 17. Board Photograph

## 10.2 PCB layout

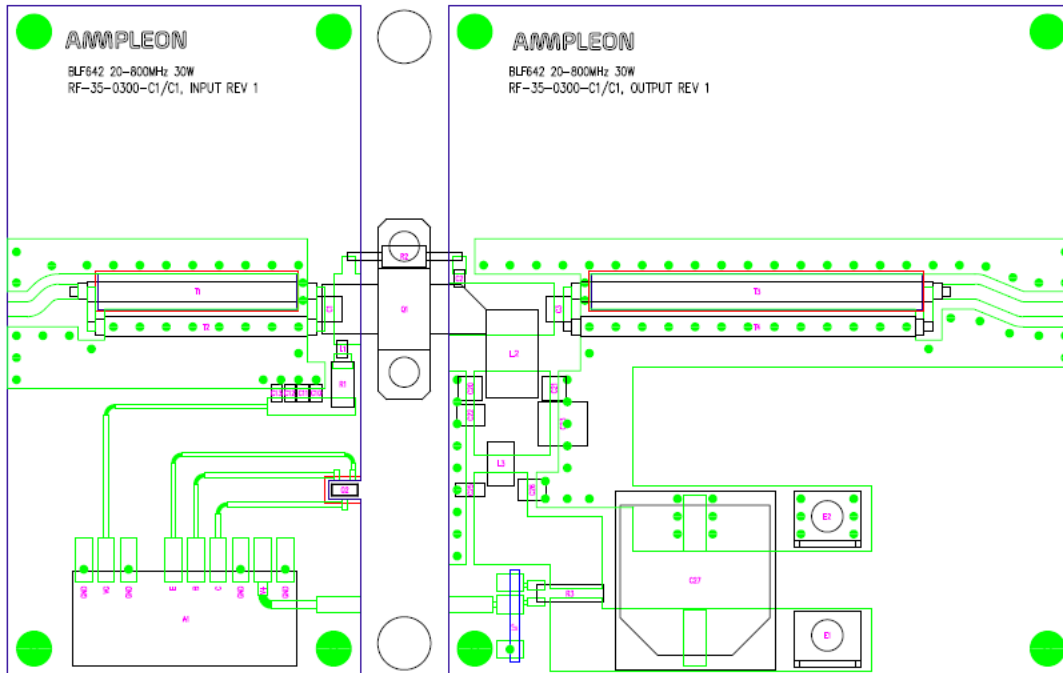


Figure 18.PCB Layout Board #AR192035



## 10.3 Bill of materials-old

Table 2. BOM

Designator	Description	Manufacturer	Part#
PCB Input PCB	Input PCB, 30mil thk. RF35	Avanti Circuits	PCB00062 Input Rev3
PCB Output PCB	Output PCB, 30 mil thk. RF35	Avanti Circuits	PCB00062 Output Rev3
A1	LDMOS bias module	Ampleon	CA-330-11
Q1	RF Transistor	Ampleon	BLP15M9S30
Q2	2N2222 NPN Transistor	Fairchild	MMBT2222
R1	10Ω 0.5W5%	Generic	
R2, R3	20 Ω 5%	IMS	NADC-2010WA20R0J
R4,R5	DNP	ATC	FR10300N0200J
R6	10 Ω 3W	Generic	
L1	17.5nH	Coilcraft	B01
L2	8 turn 18AWG wrapped onto R6	Internal	
L3	DNP		
L4	DNP		
C1,C32	1000pF	Passive Plus or ATC	1111N or 100B
C2, C4, C9, C10	100nF, 50V 10% X7R, 0805	Generic	
C3, C7, C8,C15	4.7nF,100V 5% NPO, 1210	Generic	
C13,C14	10uF,100V 10% X7R, 1206	Generic	
C11,C12	DNP	Generic	
C16	100nF,100V 10% X7R, 1210	Generic	
C17	10uF, 100V 10% X7S, 2220	TDK	C5750X7S2A106M
C5,C6,C18, 19	910pF, 500V 5%	Passive Plus or ATC	1111N or 100B
C20	470uF, 63V, alum electrolytic	Generic	
C21	2.2pF	Passive Plus or ATC	1111N or 100B
C22	30pF	Passive Plus or ATC	1111N or 100B
C23	2 x 10pF	Passive Plus or ATC	1111N or 100B
C24	DNP	Passive Plus or ATC	
C31	1000pF	Passive Plus or ATC	600F
T1	1:1 Input Balun	Micro Coax Fair-Rite	55mm PE-047 50 ohm coax + (3) Fair-Rite 2861002402 cores
T2, T3	4:1 input transformer	Micro Coax Fair-Rite	50mm UT-047-25 25 ohm coax + (2) Fair-Rite 2861002402 cores
T4, T5	4:1 output transformer	Micro Coax	3.5" UT-0C-18 18 ohm coax
T6	1:1 output balun w 1 core	Micro Coax Fair-Rite	4.1" UT-141 50 ohm coax with one BN-61-002 core

## 10.4 PCB materials

Table 3. Board Specifications

Parameter	Value
Manufacturer	Taconic
Type	RF35
Thickness	30 mils, 1oz. copper
Layers	2, top/bottom. Bottom all copper

## 10.5 Device markings

Table 4. Device Specifications

Parameter	Value
Manufacturer	Ampleon
Device	BLP15M9S30
Date Code	M1919

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