

# AR191071

BLS9G3135L(S)-115, 3100 to 3500MHz

**AMPLEON**

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Application  
Report

## v1.0 Document information

**Status** Company public

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**Abstract** Measurement results of a Class-AB design  
for the 3100 to 3500MHz band with the BLS9G3135L(S)-115

## 1. Revision History

*Table 1: Report revisions*

Revision	Date	Description	Author
1.0	20190513	Initial document	Hans Mollee

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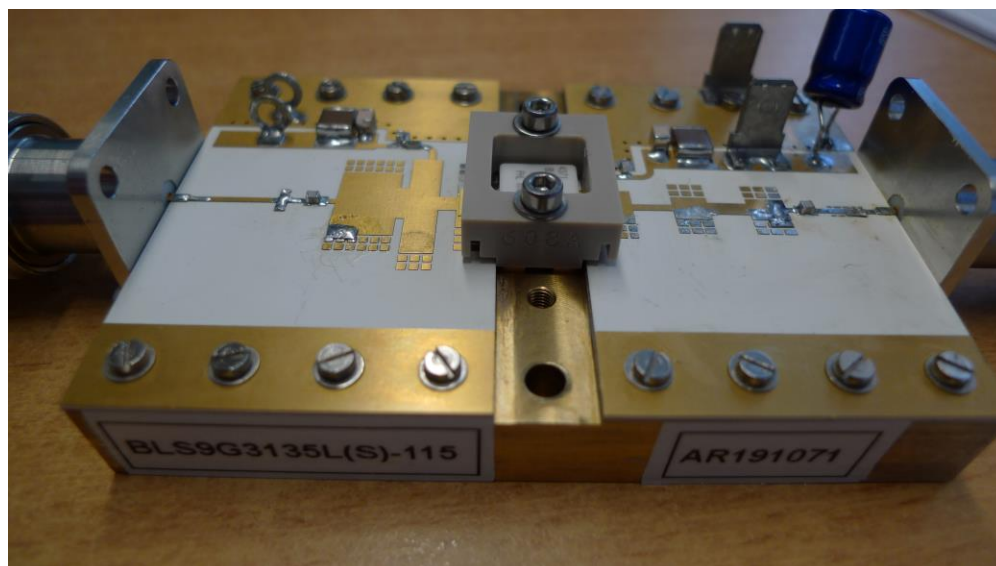
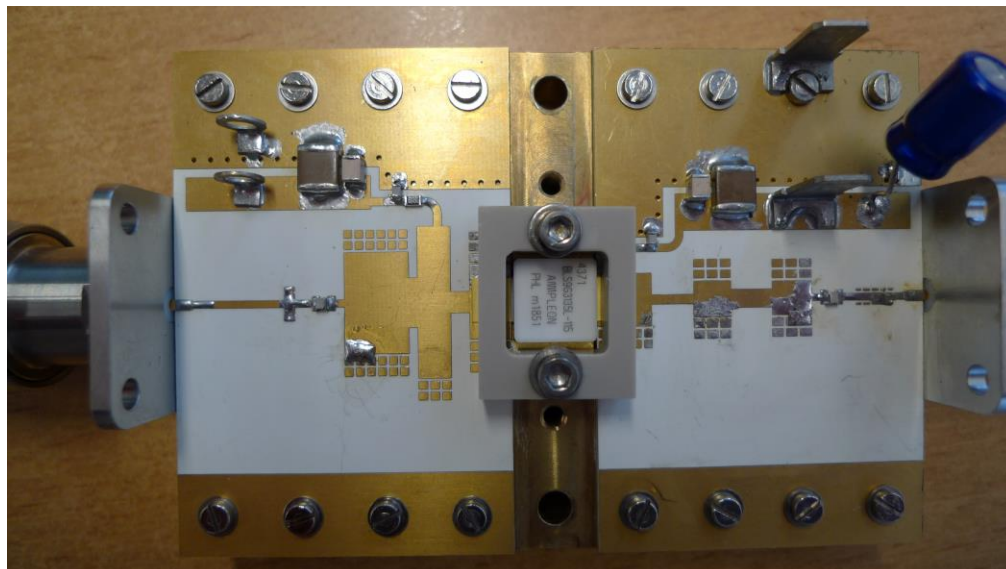
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## 5. General description

This report presents the measurement results of the Class-AB demo AR191071. The device used is a BLS9G3135L(S)-115, a 9<sup>th</sup> generation LDMOS transistor in a single ended SOT1135-package. The presented demo is tuned for the frequency band 3100 to 3500MHz.

The PCB has been designed on Rogers RO4360G2,  $h=0.61\text{mm}$ ,  $\epsilon_R=6.2$ , 35 $\mu\text{m}$  double sided copper. Supply voltage (drain-source) is 32V. Gate bias voltage is connected to the  $V_g$  terminals on the input board. To set the drain quiescent current, slowly increase  $V_{GS}$  until the  $I_{DQ}$  will be 200 mA, starting at about 1V.

When switching of the RF-pulse a spike may appear on the drain supply due to the inductance and the fall time of the pulse. When using signal with a rapid fall time this spike may become (too) large. By placing a 10 $\mu\text{F}$  SMD capacitors (C7) on the drain supply. These spikes will be reduced to virtually zero.



### 5.1 Performance Details

The pulse format used is a 300  $\mu$ s pulse with a duty cycle of 10%. The power sweep was performed up to 3 dB gain compression.

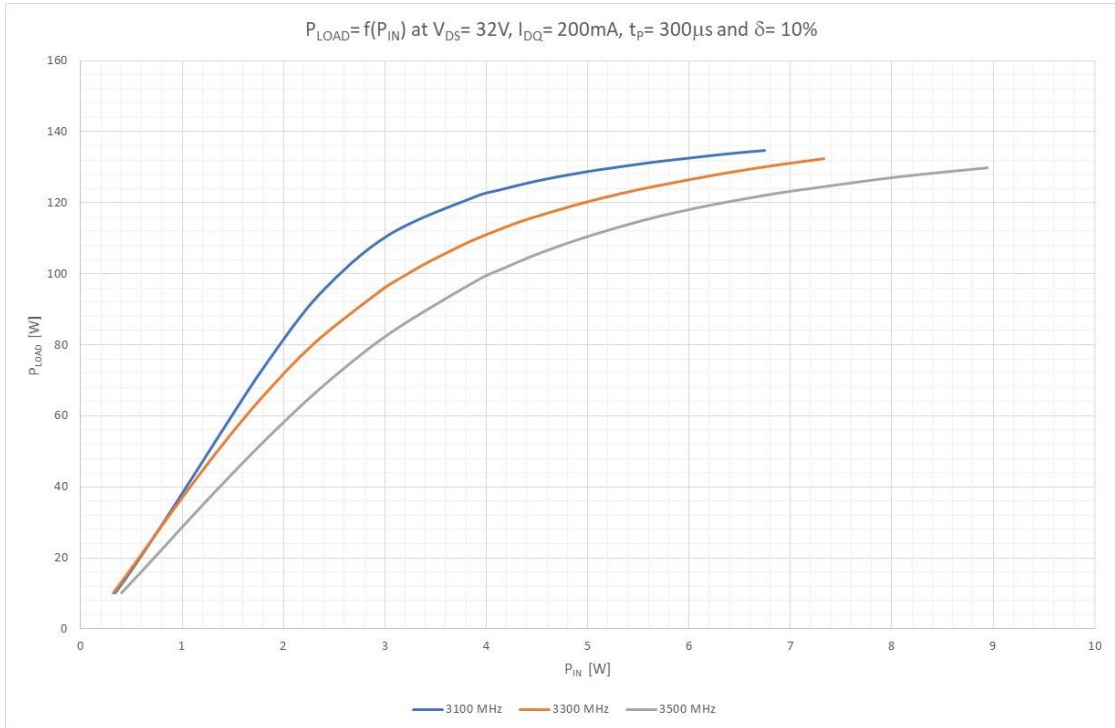


Figure 1  $P_{LOAD}$  vs  $P_{IN}$

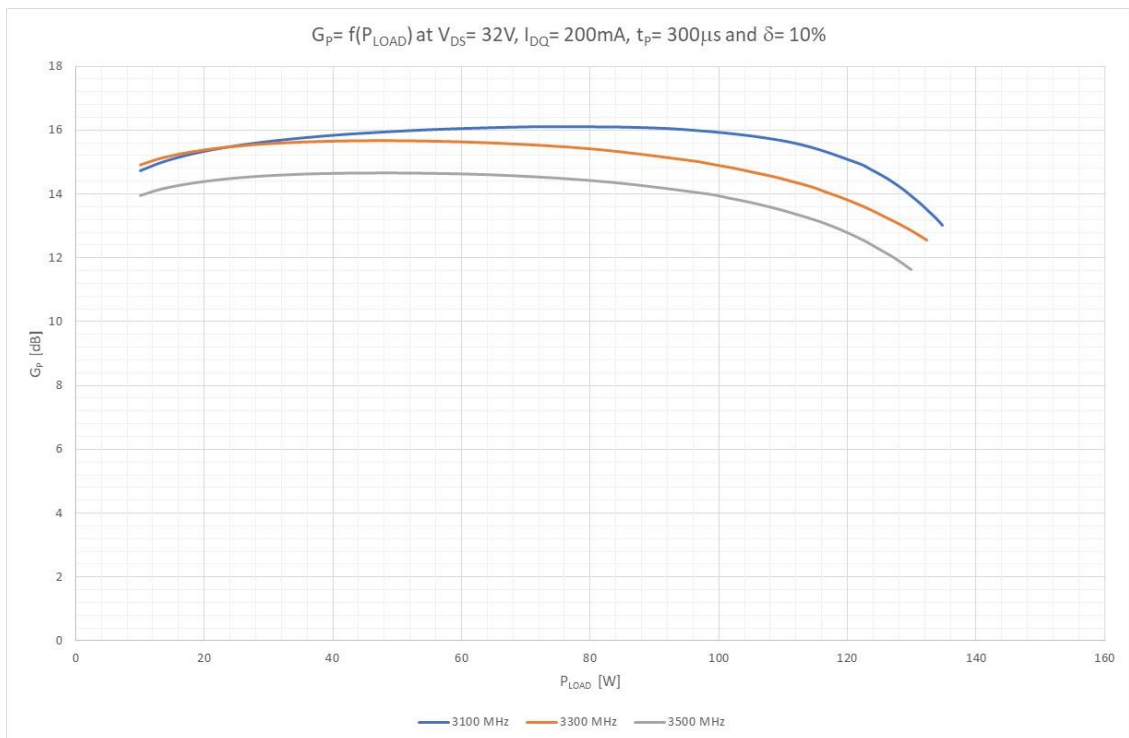


Figure 2 Gain vs  $P_{LOAD}$

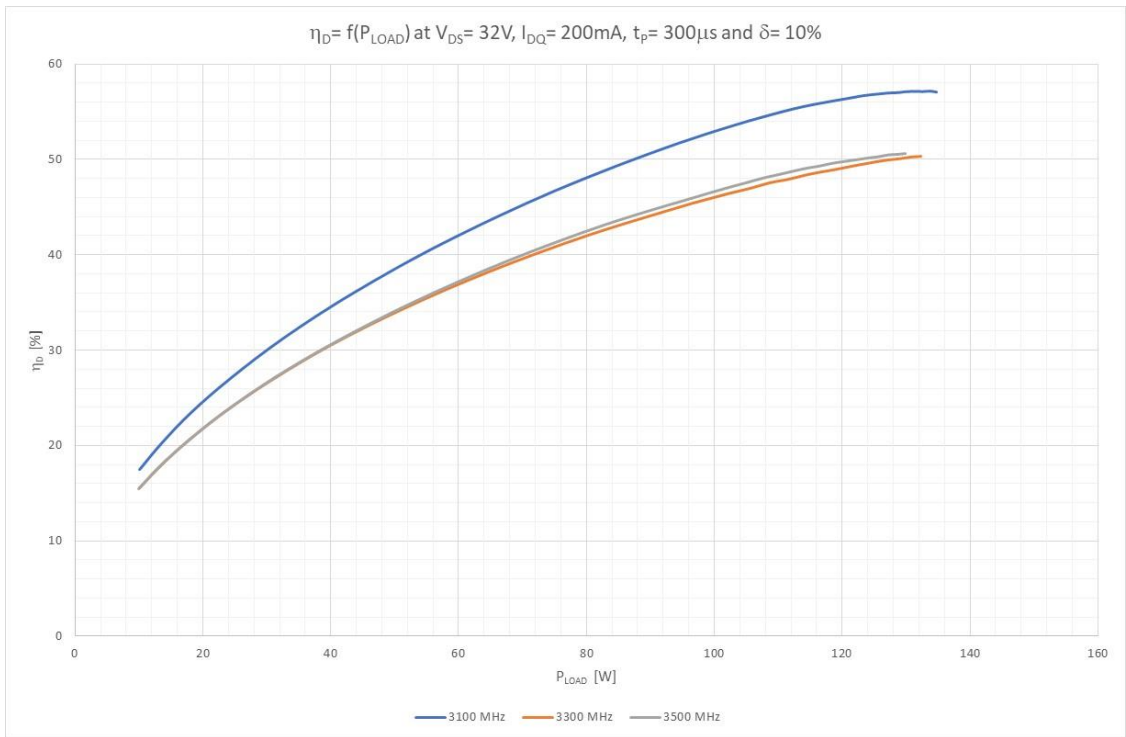


Figure 3 Drain efficiency vs P\_LOAD

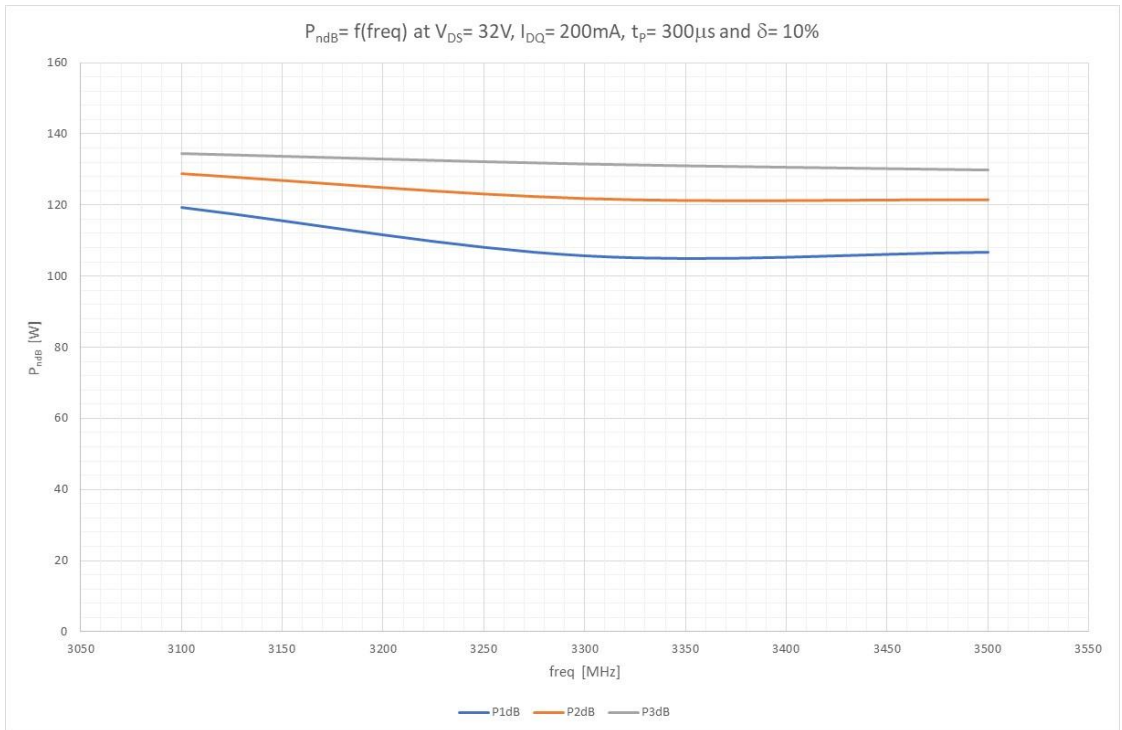


Figure 4 Compressed Power

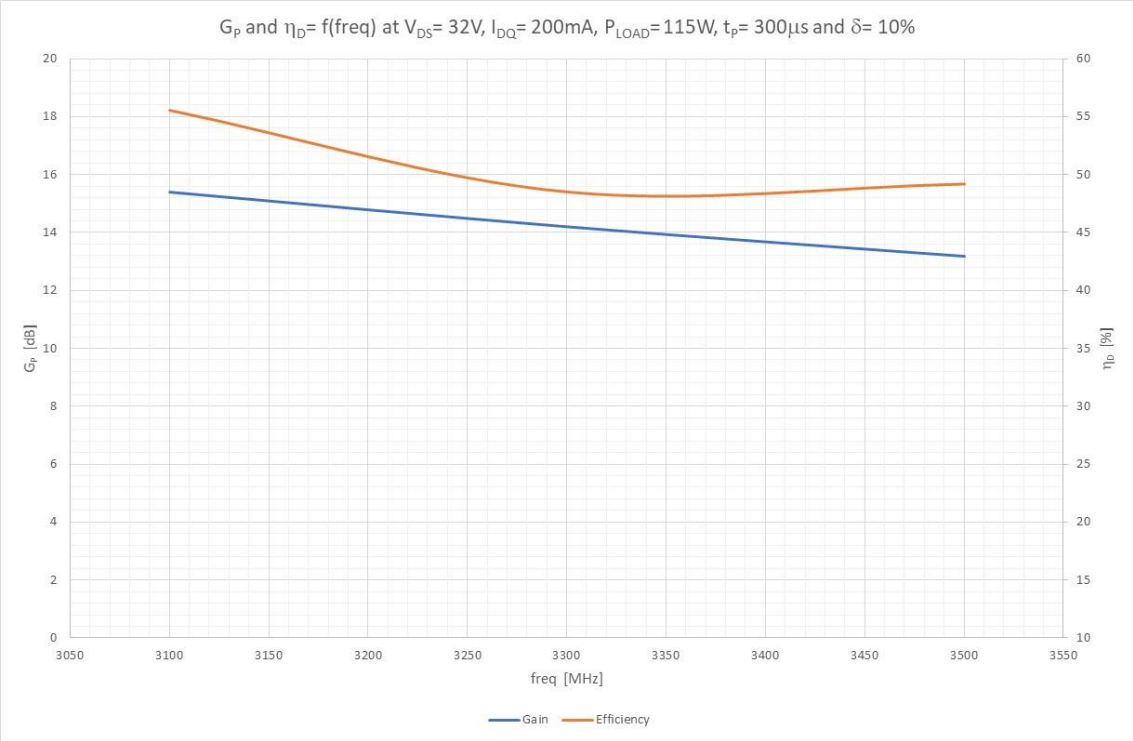
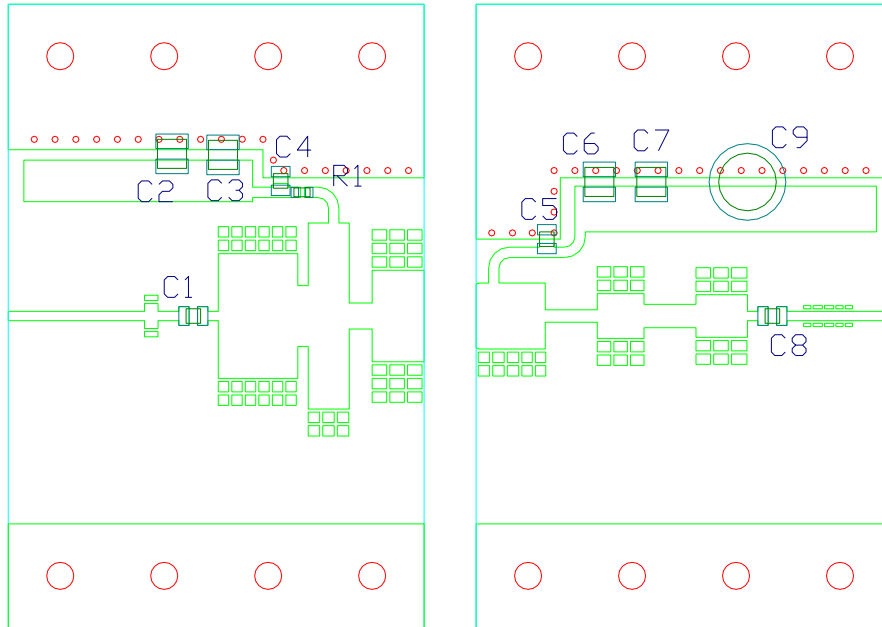


Figure 5 Performance at 115W

## 6. Hardware



### Components list application circuit.

C1	10 pF	ATC800A
C4, C5	10 pF	ATC800A
C8	10 pF	ATC100A
C3, C6	1 nF	ATC100B
C2, C7	10 $\mu$ F	Murata GRM55DR61H106KA88L
C9	100 $\mu$ F	63 V, Electrolytic capacitor
R1	5 $\Omega$	0603 SMD Resistor

PCB Material: Rogers 4360G2, thickness 0.61 mm (24 mil) or equivalent,  $\epsilon_R = 6.15$ , Cu = 35 micron

## 6.1 Board material

Table 2: Board specifications

Parameter	Value
Manufacturer	Rogers
Type	RO4360G2
Thickness	24 mil, 0.61 mm
Layers	2, top/bottom. Bottom all copper

## 6.2 Device markings

Table 3: Device specifics

Parameter	Value
Manufacturer	Ampleon
Device	BLS9G3135L-115
Marking	BLS9G3135L-115, m1851
Comments	Engineering sample



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